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DRAWING DIRECTORY

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FOR FIELD MAINTENANCE PRINT SET REFER TO K-TC-DMR11-0-1

UNIT VARIATIONS

VARIATION	TITLE
DMR11-M	DMR11 BASE OPTION, M8207-RA UPROC, M8203 LINECARD
DMR11-AP	SYSTEM INTEGRATED DMR11/RS232 FULL MODEM
DMR11-BP	SYSTEM INTEGRATED DMR11/V.35 OPTION
DMR11-CP	SYSTEM INTEGRATED DMR11/INTEGRAL MODEM
DMR11-EP	SYSTEM INTEGRATED DMR11/RS422 OPTION
DMR11-FP	SYSTEM INTEGRATED DMR11/RS423 OPTION

REVISION HISTORY	USED ON OPTION/MODEL	RESPONSIBLE ENG.	DATE	R. Bergeron 27-OCT-83	d i s i t a i	
ENG ECO NUMBER	REV	MADE BY	DATE	R. ROBICHAUD	TITLE	DRAWING DIRECTORY
ML DMR11-MK003	B	DMR11	DATE : 23 AUG 83	R. Bergeron 27-OCT-83	dmr11	DMR11
		MADE BY	DATE : 23 AUG 83	R. ROBICHAUD		
		CHECKED BY	DATE : 23 AUG 83	J. Falkowski 26-SEP-83		
		DESIGN ENGINEER	DATE : 23 AUG 83	R. Bergeron 27-OCT-83		
		PRODUCTION ENG.	DATE : 23 AUG 83	W. Ladner 27-OCT-83		
					SIZE	CODE
					I K	I DD
					I DMR11-0	I REV.
					I B	
					I SHEET	I EDIT #
					1 OF 2	0001

MK

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#	DRAWING NUMBER	DESCRIPTION	E	#	DRAWING NUMBER	DESCRIPTION	E
1	MP00911	F.M.P.S. DMR11 (MP)	-				
	K-TC-DMR11-0-1	F.M.P.S. DMR11 (TC)	-				
	K-PL-DMR11-0-0	DMR11 OPTION	E/M				
	D-IA-BC08S-0-0	I/O CABLE ASSY	E/M				
	K-PL-BC08S-0-DBP	I/O CABLE ASSY	E/M				
	K-SP-DMR11-0-2	DMR11 ENGINEERING SPECIFICATION	-				
	K-AR-CKCKREF-0-0	FLEXIBLE OPTION PLAN	-				
2	B-DD-M8207-0	MICROCONTROLLER, DMP11	-				
3	B-DD-M8203-0	MULTI DROP LINE UNIT	-				
4	B-DD-H3254-0	M8203 TEST CONN J1	-				
5	B-DD-H3255-0	M8203 TEST CONN J2	-				
6	K-DD-CKDMR11-0	CKDMR11 DRAWING DIRECTORY	-				
7	K-PL-7020872-0-0	SHIPPING LIST DMR11	-				

TYPE: E ELECTRICAL

M MECHANICAL

E/M ELECTRO/MECHANICAL

d i s i t a l

i

TITLE

DRAWING DIRECTORY

SIZE CODE DOCUMENT NUMBER REV.

K DD DMR11-0 B

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SHEET 2 OF 2 EDIT # 0001

DMR11

MK

AUTOMATED BY PRTLST.3L(40)

## PARTS LIST

SHEET A1 OF A1

LINE ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION	MIN	VARIATION	REVISION LEVEL:	QTY PER VARIATION						
								M	A	P	B	C	E	F
A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2
1	1	D-UA-M8207-0-0		M8207-RA		MICROPROCESSOR BASED SYNCHRONOUS		1	1	1	1	1	1	1
2	2	D-UA-M8203-0-0		M8203-00		DMP11 LINE UNIT, RS-449, INTEGRAL		1	1	1	1	1	1	1
3	3	D-IA-BC08S-0-0		BC08S-01		12" SHIELDED I/O CABLE, H856 EA E		1	1	1	1	1	1	1
4	4	D-UA-H3254-0-0		H3254-00		M8203 TEST CONNECTOR J1		1	1	1	1	1	1	1
5	5	D-UA-H3255-0-0		H3255-00		M8203 TEST CONNECTOR J2		1	1	1	1	1	1	1
6	6	K-PL-CKDMR11-0-0		CKDMR11-00		CABKIT REF TO K-AR-CKCKREF-0-0		1	1	1	1	1	1	1
7	7	K-PL-7020872-0-0		7020872-01		SHIPPING LIST (DMR11)		1	1	1	1	1	1	1

8. NOTE: NOTE 1: CKDMR11-00 IS A REFERENCE VARIANT ONLY. SEE K-AR-CKCKREF-0-0 FOR EXACT VARIANT APPLICATION.

REVISION HISTORY		BASIC PART NO: DMR11		DRN:	D. ZWICKER		DATE: 08-FEB-80	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION A OF A	IC	CHK'D:	G. HOVEY	19-AUG-85	TITLE		PARTS LIST				
IRH	IDMR11-MK001	A	SECTION, VARIATION INDEX				DATE: 08-FEB-80	DMR11 OPTION						
ML	IDMR11-MK003	B												
ML	IDMR11-MK004	C												

8	7	6	5	4	3	2	1																																																																																																	
<p>THIS DRAWING AND SPECIFICATIONS HEREIN, ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE COPIED OR REPRODUCED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION.</p> <p>Copyright © DIGITAL EQUIPMENT CORPORATION</p>		<p>LEGEND</p> <table border="1"> <thead> <tr> <th>NUMBER</th> <th>DIM. "X" VAR</th> <th>DIM "Y" REF</th> </tr> </thead> <tbody> <tr><td>BC08S-0L</td><td>10.0 IN ± 0.5 IN</td><td>11.0 IN ± 0.5 IN</td></tr> <tr><td>BC08S-0I</td><td>12.0 IN ± 1.0 IN</td><td>13.0 IN ± 1.0 IN</td></tr> <tr><td>BC08S-1B</td><td>14.0 IN ± 1.0 IN</td><td>15.0 IN ± 1.0 IN</td></tr> <tr><td>BC08S-1K</td><td>21.0 IN ± 1.0 IN</td><td>22.0 IN ± 1.0 IN</td></tr> <tr><td>BC08S-02</td><td>24.0 IN ± 1.0 IN</td><td>25.0 IN ± 1.0 IN</td></tr> <tr><td>BC08S-2K</td><td>33.0 IN ± 1.0 IN</td><td>34.0 IN ± 1.0 IN</td></tr> <tr><td>BC08S-5L</td><td>70.0 IN ± 2.0 IN</td><td>71.0 IN ± 2.0 IN</td></tr> <tr><td>BC08S-07</td><td>7.0 FT ± 2.0 IN</td><td>7.0 FT 1.0 IN ± 2.0 IN</td></tr> <tr><td>BC08S-08</td><td>8.0 FT ± 2.0 IN</td><td>8.0 FT 1.0 IN ± 2.0 IN</td></tr> <tr><td>BC08S-09</td><td>9.0 FT ± 2.0 IN</td><td>9.0 FT 1.0 IN ± 2.0 IN</td></tr> <tr><td>BC08S-10</td><td>10.0 FT ± 3.0 IN</td><td>10.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-11</td><td>11.0 FT ± 3.0 IN</td><td>11.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-12</td><td>12.0 FT ± 3.0 IN</td><td>12.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-13</td><td>13.0 FT ± 3.0 IN</td><td>13.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-14</td><td>14.0 FT ± 3.0 IN</td><td>14.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-15</td><td>15.0 FT ± 3.0 IN</td><td>15.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-16</td><td>16.0 FT ± 3.0 IN</td><td>16.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-17</td><td>17.0 FT ± 3.0 IN</td><td>17.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-18</td><td>18.0 FT ± 3.0 IN</td><td>18.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-19</td><td>19.0 FT ± 3.0 IN</td><td>19.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-20</td><td>20.0 FT ± 3.0 IN</td><td>20.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-25</td><td>25.0 FT ± 3.0 IN</td><td>25.0 FT 1.0 IN ± 3.0 IN</td></tr> <tr><td>BC08S-30</td><td>30.0 FT ± 7.2 IN</td><td>30.0 FT 1.0 IN ± 7.2 IN</td></tr> <tr><td>BC08S-35</td><td>35.0 FT ± 8.4 IN</td><td>35.0 FT 1.0 IN ± 8.4 IN</td></tr> <tr><td>BC08S-50</td><td>50.0 FT ± 12.0 IN</td><td>50.0 FT 1.0 IN ± 12.0 IN</td></tr> <tr><td>BC08S-03</td><td>3.0 FT ± 1.0 IN</td><td>3.0 FT 1.0 IN ± 1.0 IN</td></tr> <tr><td>BC08S-05</td><td>5.0 FT ± 2.0 IN</td><td>5.0 FT 1.0 IN ± 2.0 IN</td></tr> <tr><td>BC08S-05</td><td>150.0 FT ± 2%</td><td>150.0 FT 1.0 IN ± 2%</td></tr> <tr><td>BC08S-05</td><td>250.0 FT ± 2%</td><td>250.0 FT 1.0 IN ± 2%</td></tr> <tr><td>BC08S-2F</td><td>30.0 IN ± 1.0 IN</td><td>31.0 IN ± 1.0 IN</td></tr> <tr><td>BC08S-1C</td><td>15.0 IN ± 1.0 IN</td><td>16.0 IN ± 1.0 IN</td></tr> <tr><td>BC08S-04</td><td>4.0 FT ± 1.0 IN</td><td>4.0 FT 1.0 IN ± 1.0 IN</td></tr> </tbody> </table>		NUMBER	DIM. "X" VAR	DIM "Y" REF	BC08S-0L	10.0 IN ± 0.5 IN	11.0 IN ± 0.5 IN	BC08S-0I	12.0 IN ± 1.0 IN	13.0 IN ± 1.0 IN	BC08S-1B	14.0 IN ± 1.0 IN	15.0 IN ± 1.0 IN	BC08S-1K	21.0 IN ± 1.0 IN	22.0 IN ± 1.0 IN	BC08S-02	24.0 IN ± 1.0 IN	25.0 IN ± 1.0 IN	BC08S-2K	33.0 IN ± 1.0 IN	34.0 IN ± 1.0 IN	BC08S-5L	70.0 IN ± 2.0 IN	71.0 IN ± 2.0 IN	BC08S-07	7.0 FT ± 2.0 IN	7.0 FT 1.0 IN ± 2.0 IN	BC08S-08	8.0 FT ± 2.0 IN	8.0 FT 1.0 IN ± 2.0 IN	BC08S-09	9.0 FT ± 2.0 IN	9.0 FT 1.0 IN ± 2.0 IN	BC08S-10	10.0 FT ± 3.0 IN	10.0 FT 1.0 IN ± 3.0 IN	BC08S-11	11.0 FT ± 3.0 IN	11.0 FT 1.0 IN ± 3.0 IN	BC08S-12	12.0 FT ± 3.0 IN	12.0 FT 1.0 IN ± 3.0 IN	BC08S-13	13.0 FT ± 3.0 IN	13.0 FT 1.0 IN ± 3.0 IN	BC08S-14	14.0 FT ± 3.0 IN	14.0 FT 1.0 IN ± 3.0 IN	BC08S-15	15.0 FT ± 3.0 IN	15.0 FT 1.0 IN ± 3.0 IN	BC08S-16	16.0 FT ± 3.0 IN	16.0 FT 1.0 IN ± 3.0 IN	BC08S-17	17.0 FT ± 3.0 IN	17.0 FT 1.0 IN ± 3.0 IN	BC08S-18	18.0 FT ± 3.0 IN	18.0 FT 1.0 IN ± 3.0 IN	BC08S-19	19.0 FT ± 3.0 IN	19.0 FT 1.0 IN ± 3.0 IN	BC08S-20	20.0 FT ± 3.0 IN	20.0 FT 1.0 IN ± 3.0 IN	BC08S-25	25.0 FT ± 3.0 IN	25.0 FT 1.0 IN ± 3.0 IN	BC08S-30	30.0 FT ± 7.2 IN	30.0 FT 1.0 IN ± 7.2 IN	BC08S-35	35.0 FT ± 8.4 IN	35.0 FT 1.0 IN ± 8.4 IN	BC08S-50	50.0 FT ± 12.0 IN	50.0 FT 1.0 IN ± 12.0 IN	BC08S-03	3.0 FT ± 1.0 IN	3.0 FT 1.0 IN ± 1.0 IN	BC08S-05	5.0 FT ± 2.0 IN	5.0 FT 1.0 IN ± 2.0 IN	BC08S-05	150.0 FT ± 2%	150.0 FT 1.0 IN ± 2%	BC08S-05	250.0 FT ± 2%	250.0 FT 1.0 IN ± 2%	BC08S-2F	30.0 IN ± 1.0 IN	31.0 IN ± 1.0 IN	BC08S-1C	15.0 IN ± 1.0 IN	16.0 IN ± 1.0 IN	BC08S-04	4.0 FT ± 1.0 IN	4.0 FT 1.0 IN ± 1.0 IN	<p>NOTES:</p> <ol style="list-style-type: none"> <li>CONNECTORS P1 AND P2, ARE TO BE WIRED POINT TO POINT.</li> <li>CONNECTOR LEGEND IDENTIFICATION TO BE PLACED ON SHIELD SIDE AT EACH END OF CABLE ASSEMBLY.</li> <li>MUST BE ASSEMBLED TO PROCESS SPECIFICATION 7606485-0-0.</li> <li>INSPECTION AND TEST STAMPS TO BE PLACED AT EACH END OF THE CABLE ASSEMBLY.</li> <li>THE NUMBER BC08S MUST BE ON THE CABLE IN CONJUNCTION WITH CONNECTOR LEGEND IDENTIFICATION.</li> <li>ADD ITEM 3 APPROX 4 IN FRCPA CONNECTOR P1. EITHER I.D. LABEL 3616929-00 OR 3616073-00 MAY BE USED. PLACE ON SMOOTH SIDE OF CABLE.</li> <li>IF MARKER STRIPE APPEARS ON CABLE, ITEM 1, IT IS TO BE POSITIONED BETWEEN A-A.</li> <li>THIS LABEL MAY BE REQUIRED PER UL REQUIREMENT FOR INTERCONNECTING SYSTEMS.</li> </ol>	
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SEE NOTE 7

2 SHIELD CUT AWAY (2 PLACES)

1

PI A

SEE NOTES 2

P2 B

SEE NOTE 2

SEE NOTES 6 & 8

3

VIEW A-A CONN. LEGEND REF

VIEW B-B CONN. LEGEND REF

DIM 'X'

DIM 'Y' REF

RIB SIDE

REVISION

CHK	CHANGE NO.	REV.
104	1	1
S. HOLMES - 23 JUN 76		
REVISED AND REHAWN		
W. FISHER 14 JUN 76		
S. HOLMES		
S. HOLMES - 23 JUN 76		
D.C. BCOBS-0001 N		
D. CHIASSON 19 JUL 76		
E. J. NEUMAYER		
F. J. NEUMAYER 7-30-76		
G. BC08S-00012 P		
H. BC08S-00013		
I. BC08S-00014		
J. BC08S-00015		
K. BC08S-00016		
L. BC08S-00017		
M. BC08S-00018		
N. BC08S-00019		
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OO. BC08S-00171		
PP. BC08S-00172		
QQ. BC08S-00173		
RR. BC08S-00174		
TT. BC08S-00175		
UU. BC08S-00176		
VV. BC08S-00177		
WW. BC08S-00178		
XX. BC08S-00179		
YY. BC08S-00180		
ZZ. BC08S-00181		
AA. BC08S-00182		
BB. BC08S-00183		
CC. BC08S-00184		
DD. BC08S-00185		
EE. BC08S-00186		
FF. BC08S-00187		
GG. BC08S-00188		
HH. BC08S-00189		
II. BC08S-00190		
JJ. BC08S-00191		
KK. BC08S-00192		
LL. BC08S-00193		
MM. BC08S-00194		
NN. BC08S-00195		
OO. BC08S-00196		
PP. BC08S-00197		
QQ. BC08S-00198		
RR. BC08S-00199		
TT. BC08S-00200		
UU. BC08S-00201		
VV. BC08S-00202		
WW. BC08S-00203		
XX. BC08S-00204		
YY. BC08S-00205		
ZZ. BC08S-00206		
AA. BC08S-00207		
BB. BC08S-00208		
CC. BC08S-00209		
DD. BC08S-00210		
EE. BC08S-00211		
FF. BC08S-00212		
GG. BC08S-00213		
HH. BC08S-00214		
II. BC08S-00215		
JJ. BC08S-00216		
KK. BC08S-00217		
LL. BC08S-00218		
MM. BC08S-00219		



AUTOMATED BY FRTLST.SR(55)

P A R T S      L I S T

4 NOTE: 1. EITHER I.D. CAB: E 3616989-00 OR 3616073-00 MAY BE USED.  
5 NOTE: 2. PLACE ON SMOOTH SIDE OF CABLE.  
6 NOTE: 3. QUANTITY OF ITEM 1, VARIATIONS -0L, -01, -1B, -1K, -02, -2K, -5L, -2F, AND -1C AP  
7 NOTE: SHOWN IN INCHES.  
8 NOTE: 4. QUANTITY OF ITEM 1, ALL OTHER VARIATIONS ARE SHOWN IN FEET. NOTE THAT THE  
9 NOTE: QTY OF THESE VARIATIONS DO NOT INCLUDE ALLOWANCE FOR CONNECTORS.

!A-PL-DR8-EA-0 ! BC08SZ.FLS ! 7 !  
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AUTOMATED BY FRTLST,5RCESS

## P A R T S      L I S T

SHEET C1 OF C1

LINE ITEM TOP DOCUMENT

PART NUMBER REV DESCRIPTION

## VARIATION REVIEW LEVEL

4 NOTE: 1. EITHER I.D. CABLE 3616989-00 OR 3416023-00 MAY BE USED.  
5 NOTE: 2. PLACE ON SMOOTH SIDE OF CABLE.  
6 NOTE: 3. QUANTITY OF ITEM 1, VARIATIONS -0L, -01, -1B, -1K, -02, -2K, -5L, -2F, AND -1C ARE  
7 NOTE: SHOWN IN INCHES.  
8 NOTE: 4. QUANTITY OF ITEM 1, ALL OTHER VARIATIONS ARE SHOWN IN FEET. NOTE THAT THE  
9 NOTE: QTY OF THESE VARIATIONS DO NOT INCLUDE ALLOWANCE FOR CONNECTORS.

## REFUSION RIS

REVISION HISTORY			BASIC PART NO:	BC08S	DRN:	F. LASPINA	DATE: 15-NOV-83	D	I	G	I	T	A	L
ENGI	ECO NUMBER	REV	SECTION C	OF C										
INITIAL	W	Y	SECTION	VARATATION	INDEX	CHK'D:	L. GILBERT	DATE: 15-NOV-83						
ML	BC08S-PN019				CA101,01,18,1K,02,2K,									
AF	BC08S-MR020	Z			5L,07,08,09,10,11									
					CB112,13,14,15,16,17,	DES.ENG:	E. KING	DATE: 15-NOV-83						
					18,19,20,25,30,35									
					EC150,03,05,AS,BS,2F,									
					1C,04	RESP.ENG.:	E. KING	DATE: 15-NOV-83						
					ED3									
					ED3	MFG.ENG.:	J. DEBLASIO	DATE: 15-NOV-83						
					EF3	ASSEMBLY NUMBER:								
						DR-TA-BC08S-0-0		TOP DOCUMENT NUMBER:						
								IA-PL-DR8-EA-0						
									FILE NAME:					
									BC08S2.PLS					

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DRAWING NO.	NO. SHTS	PART NO.	DESCRIPTION	REVISIONS
		M8207-00	DMP11 MICROCONTROLLER	T1 T2 - T4 T5
		M8207-RA	DMP11 MICROCONTROLLER	T1 T2 T3 T4 T5
		M8207-YA	DMP11 MICROCONTROLLER	T1 T2 - T4 T5
D-UA-M8207-0-0	1		DMP11 MICROCONTROLLER	
K-PL-M8207-0-0	3		DMP11 MICROCONTROLLER	P N1 - R R
K-PL-M8207-0-DBP	3		DMP11 MICROCONTROLLER	R N1 - S IN
D-CS-M8207-0-1	21		DMP11 MICROCONTROLLER	- - - - T
K-PC-M8207-0-DBI	-		DMP11 MICROCONTROLLER	P M1 M1 R R
K-PC-M8207-0-DBC	-		IPC DESIGN DATA BASE	E - - F F
D-UA-M8207-0-2	2		IPC DESIGN DATA BASE	- D1 D1 - -
K-PL-M8207-0-2	3		DMP11 MICROCONTROLLER W.I.	- - A - -
			DMP11 MICROCONTROLLER W.I.	- - A - -
		5012845-00	ETCH BOARD	
		5012845-01	ETCH BOARD WIRE INK	E D - F F
K-DD-5012845-0-0	1		DRAWING DIRECTORY	- - D - -
				A A A B B

NOTES:

- T1-REV EP1, ETCH BD, RE-LAYOUT.
- T2-REV D ETCH, BD, WITH ECO WIRES.
- T3-REV D ETCH, WITH WIRE INK FOR M8207-RA VARIATION ONLY.

R	P	P	P	R	S
R					
E					
V					
R					
E	H	E	M	M	M
V	I	C	K	K	K
I	S	O	O	O	O
S	T		1	1	1
I	O	N	3	3	4
O	R	O			5
N	Y				
D	0	0	0	0	0
A	9	9	9	2	9
T	/	/	/	/	/
E	8	8	8	8	8
	3	3	3	5	8

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DRN: B. FRASER

DATE: 13-MAR-79

d i g i t a l

CHK'D: R. KOPPENAL

DATE: 14-MAR-79

TITLE  
DMP11 MICROCONTROLLER

DES.ENG.: P. ALOISI

DATE: 14-MAR-79

SHEET 1 OF 1 EDIT:  
6

RESP.ENG.: M. LAWRENCE

DATE: 19-SEP-85

DOCUMENT NUMBER

DD FILENAME: M8207S.DDF

MFG.ENG.: G. ARMBRUSTER

DATE: 14-MAR-79

SIZE CODE NUMBER REV  
K DD M8207-0-0 S



LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VAR/REV			REFERENCE DESIGNATORS
					00	RA	YA	
1	1	10-D-MD-5012845-0-0	50-12845-00	M8207	1	1	1	
2	2		10-00042-00	1000 PFD 100V	+/- 5%	200PP	3	3,3
3	3		10-10031-00	0.15 MFD 50V	+/-10%	M.POL	1	1,1
4	4		10-00076-00	39 MFD 10V	+/-10%	SOL	1	1,1
5	5	#NOTE 1	10-12084-01	8.0 MFD 25V	+75/-10%	85	7	7,7
6	6		10-00017-00	100 PFD 300V	+/- 5%	200PP	3	3,3
7	7		10-00026-00	680 PFD 100V	+/- 5%	200PP	2	2,2
8	8		10-00011-00	47 PFD 100V	+/- 5%	200PP	1	1,1
9	9		10-02427-00	*** THIS ITEM IS NOT USED ***			-	C18
10	10		10-13466-11	0.22 MFD 50V	+80/-20%	Z5U C	25	25,25
11	11		10-12784-00	0.047MFD 50V	+80/-20%	Z5U C	41	41,41
12	12		12-09941-02	PCB, HEADER 40PIN(2X20)	1.00CC	90D	1	1,1
13	13		12-12385-04	SKT, IC 24PIN DIP GOLD	SOLD		6	6,6
14	14		12-16988-02	HANDLE, MODULE, HEX TWO EJECTORS			1	1,1
15	15		12-09838-00	SKT, IC 16PIN DIP GOLD			1	1,1
16	16		13-00229-00	100.0 .25 W 5.0 %	CF		6	6,6
17	17		13-00365-00	1.0 K .25 W 5.0 %	CF		17	17,17
18	18		13-00271-00	220.0 .25 W 5.0 %	CF		1	1,1
19	19		13-01890-00	560.0 .25 W 5.0 %	CF		1	1,1
20	20		13-01322-00	180.0 .25 W 5.0 %	CF		5	5,5
21	21		13-09419-00	19.60 K .25 W 1.0 %	RN55D-F10		1	1,1
22	22		13-02514-00	39.0 K .25 W 5.0 %	CF		1	1,1
23	23		13-01317-00	10.0 .25 W 5.0 %	CF		3	3,3
24	24			*** THIS ITEM IS NOT USED ***			-	
25	25		13-03179-00	8.20 K .25 W 5.0 %	CF		2	2,2
26	26		13-02177-00	47.0 K .25 W 5.0 %	CF		2	2,2
27	27		13-00309-00	390.0 .25 W 5.0 %	CF		2	2,2
28	28		23-56F4 -00	F4-01			-	E1
29	29		21-14523-00	4K MOS RAM 55NS 18PIN			8	8,8
								E6-E12,E27

REVISION HISTORY			KPL MODULE FORMAT		SECTION A OF A	DRN: J.PLANTE	D I G I T A L				
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 14-MAR-79	CHK'D: J.PELLETIER	PARTS LIST				
PA	M8207-MK002	D	[A]	00,RA,YA	[M]	DATE: 14-MAR-79	CHK'D: J.PELLETIER	TITLE DMP11 MICROCONTROLLER			
PA	M8207-MK003	E	[B]		[N]			DOCUMENT NUMBER			
PA	M8207-MK005	H	[C]		[P]	DES.ENG: P.ALOISI	SIZE	CODE	NUMBER	REV	
PA	M8207-MK006	J	[D]		[Q]	DATE: 14-MAR-79					
RH	M8207-MK007	K	[E]		[R]						
PO	M8207-MK009	L	[F]		[S]	RESP.ENG.: M.LAWRENCE					
FB	M8207-MK010	M	[H]		[T]	DATE: 09-JAN-84	K	PL	M8207-0-DBP	T	
ML	M8207-MK011	N	[J]		[V]						
ML	M8207-MK012	P	[K]		[W]	MFG.ENG: W.BROOKE					
ML	M8207-MK013	R	[L]		[Y]	DATE: 09-JAN-84					
ML	M8207-MK014	S									
RB	M8207-MK015	T	BASIC PART NUMBER: M8207		ASSEMBLY NUMBER: D-UA-M8207-0-0	TOP DOCUMENT NUMBER: K-DD-M8207-0-0	EDIT # 6				
			FILE NAME: M8207T.PLS								

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## PARTS LIST

SHEET A2 OF A3

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VAR/REV			REFERENCE DESIGNATORS
					00	RA	YA	
					T5	T5	T5	
30	30	19-11579-00		/REPLACED BY 19-14987-00	10	10	10	E50,E51,E61,E62,E91-E93,E101, CONT E102,E115
31	31	19-13294-00		93S16 COUNTER, SYNCH UP BIN	7	7	7	E15-E18,E24-E26
32	32	19-14214-00		LS374 FF-D OCTAL EDGE TRIG	4	4	4	E20-E23
33	33	19-12847-00		LS157 MUX 1 OF 2(QUAD)	9	9	9	E29,E31,E40,E42,E52,E63,E70, CONT E79,E94
34	34	19-13671-00		74S374 FF-D, OCTAL, TR1 STATE	3	3	3	E30,E41,E54
35	35	23-202A1-00	A1-07	74S138 DECODER/DEMUX 3-8 LI	1	1	1	E32
36	36	19-11675-00		74S251 MUX 1 OF 8 TRI-STA	4	4	4	E33,E56,E80,E96
37	37	19-12728-00		LS174 FF-D HEX W/CLEAR	8	8	8	E34-E36,E46-E48,E57,E58
38	38	19-12697-00		LS194A SHIFT REG., 4BIT BI-D	3	3	3	E37,E43,E97
39	39	19-12696-00		74S240 OCTAL BUFFER, INVERTI	2	2	2	E38,E49
40	40	19-13462-00		A1-07	4	4	4	E39,E88,E106,E113
41	41	23-120A1-00		74S20 NAND GATE-DUAL 4INPU	1	1	1	E44
42	42	19-10539-00		LS257 MUX 1 OF 2 (QUAD)	1	1	1	E45
43	43	19-12647-00		74S151 MUX 1 OF 8	2	2	2	E53,E64
44	44	19-10956-00		74S00 NAND GATE-QUAD 2IN	1	1	1	E55
45	45	19-10532-00		74S158 MUX 1 OF 2 (QUAD)	2	2	2	E59,E108
46	46	19-10549-00		A1-07	1	1	1	E60
47	47	23-121A1-00		74S181 ALU-4BIT	1	1	1	E65
48	48	19-10531-00		74S175 FF-D QUAD COMMON CLO	2	2	2	E66,E74
49	49	19-10957-00		74S157 MUX 1 OF 2 (QUAD)	4	4	4	E67,E75,E89,E99
50	50	19-10548-00		LS123 ONE SHOT-DUAL, RETRIG	3	3	3	E68,E83,E86
51	51	19-12837-00		82S112 MEMORY READ/WRITE, 3	3	3	3	E69,E129,E132
52	52	19-12741-00		LS175 FF-D QUAD	4	4	4	E71-E73,E78
53	53	19-12853-00		DEC 7450 A-O-I XPNDBLE GATE-D	1	1	1	E76
54	54	19-05580-00		R. NET JUMPER PRIOR. 5 - 6 SPE	2	2	2	E81,E117
55	55	13-18784-01		74S74 FF-D DUAL, EDGE TRIGG	1	1	1	E77
56	56	19-10544-00		74S189 MEMORY READ/WRITE	4	4	4	E82,E105,E109,E121
57	57	19-12661-00		74S11 AND GATE-TRIPLE 3INP	2	2	2	E84,E90
58	58	19-10537-00		LS153 MUX 1 OF 4 (DUAL)	2	2	2	E95,E119
59	59	19-12845-00		DEC 8881 NAND GATE-QUAD 2IN 0	1	1	1	E98
60	60	19-09705-00		LS138 DECODER-THREE INPUT,	1	1	1	E100,E114
61	61	19-12842-00		LS08 AND GATE-QUAD 2IN,PO	1	1	1	E104
62	62	19-12805-00		74S10 NAND GATE-TRIPLE 3IN	1	1	1	E103
63	63	19-10536-00		74S51 AND-OR GATE-INVERT D	1	1	1	E107
64	64	19-11712-00		74S64 A-O-I GATE 4-2-3-2	2	2	2	E110,E118
65	65	19-10542-00		DC 013 UNIBUS INTERRUPT-BIP	1	1	1	E111
66	66	19-14438-00		LS74 FF-D DUAL, EDGE TRIGG	2	2	2	E112,E124
67	67	19-12824-00		74S112 FF-JK DUAL, EDGE TRIG	2	2	2	E116,E120
68	68	19-10545-00		DM 8136 COMPARATOR-6BIT UNIF	3	3	3	E122,E134,E135
69	69	19-12395-00		OSCILLATOR, XTAL 33.330 MHZ	2	2	2	E125,E126
70	70	18-11660-05		LS112 FF-JK, DUAL, EDGE TRIG	1	1	1	E123
71	71	19-12834-00		DEC 7402 NOR GATE-QUAD 2IN	1	1	1	E130
72	72	19-09004-00		74S08 AND GATE-QUAD 2IN,PO	1	1	1	E131
73	73	19-12389-00		SW,DIP 08POS/1PST 5VDC100MA F	1	1	1	E133
74	74	12-11164-04		12-11164-06	1	1	1	E28
75	75	12-11164-06		SW,DIP 10POS/1PST 5VDC100MA F	1	1	1	E127
76	76	13-00005-03		R. NET 10.OK-13 5.0	1	1	1	E128
77	77	90-00024-01		*** THIS ITEM IS NOT USED ***	-	-	-	

D I G I T A L ! TITLE

DMP11 MICROCONTROLLER

SECTION A OF A

SIZE ! CODE ! DOCUMENT NUMBER ! REV !  
K ! PL ! M8207-0-DBP ! T !

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	PARTS LIST			QTY	PER	VAR/REV	REFERENCE DESIGNATORS
					DESCRIPTION	00	RA	YA			
						T5	T5	T5			
78	78	SEE NOTE #	78	12-21142-01	TERM, TAB .250TAB	2	2	2	TP1,TP2		
79	79			90-09000-00	EYELET, ROLLED 0.1210DX0.156	14	14	14			
80	80			90-09185-00	JUMPER, WIRE, INSULATED, BLACK B	10	-	-	W1-W10		
				CONT		-	6	-	W1-W3,W5,W8,W10		
						-	-	6	W1,W2,W4,W6,W7,W9		
81	81			23-57F4 -00	F4-01	-	-	1	E2		
82	82			23-58F4 -00	F4-01	-	-	1	E3		
83	83			23-59F4 -00	F4-01	-	-	1	E4		
84	84			23-62F4 -00	F4-01	-	-	1	E5		
85	85			23-63F4 -00	F4-01	-	-	1	E14		
86	86				*** THIS ITEM IS NOT USED ***	-	-	-			
87	87	#NOTE 2		12-11164-00	SW,DIP 4POS/1PST 5VDC100MA F	1	1	1	E85		
88	88				*** THIS ITEM IS NOT USED ***	-	-	-			
89	89			23-127F3-00	F3-03	-	1	-	E1		
90	90			23-100F3-00	F3-03	-	1	-	E2		
91	91			23-101F3-00	F3-03	-	1	-	E3		
92	92			23-128F3-00	F3-03	-	1	-	E4		
93	93			23-103F3-00	F3-03	-	1	-	E5		
94	94			23-104F3-00	F3-03	-	1	-	E14		
95	95				*** THIS ITEM IS NOT USED ***	-	-	-			
96	96			37-00689-10	PKG UNIVERSAL MODULE 10 PACK	1	1	1			
97	97				*** THIS ITEM IS NOT USED ***	-	-	-			

78 NOTE: 90-07112-00 MAY BE USED INSTEAD OF 12-21142-01 AT MANUFACTURERS DISCRETION.

1 GEN: NOTE 1. 1004813-00 MAY BE USED INSTEAD OF 1012084-01.  
 2 GEN: NOTE 2. 1211164-01 MAY BE USED INSTEAD OF 1211164-00

SIZE	CODE	DOCUMENT NUMBER	REV
K	PL	M8207-0-DBP	T

D I G I T A L ! TITLE

DMP11 MICROCONTROLLER

SECTION A OF A

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ALU f CODE	
25 COMP SUB	(A-B) 1110
IS COMP SUB	(A-B-1) 1111
ADD	(A,B) 0000
ADD W/C	(A,B,C) 0001
SUB W/C	(A-B-C) 0010
INC A	(A+1) 0111
A PLUS C	(A,C) 0100
2A	(A,A) 0101
2A W/C	(A,A,C) 0110
DECA	(A-1) 0111
SEL A	(A) 1000
SEL B	(B) 1001
A OR B	(A+B) 1010
A AND B	(AB) 1011
A OR B	(A+B) 1100
A XOR B	A $\oplus$ B 1101

MOV INST  
CLOCK C

NOTES

- C WILL ASSERT FOR A CARRY (O'FLOW) DURING ANY ADD, INC, PLUS, ETC. AND FOR A SUB, DEC.... WHEN THERE IS NOT A BORROW OR (SIGN CHANGE) ADD: C + INDICATES CARRY OR O'FLOW SUB: C + INDICATES BORROW OR SIGN CHANGE.
- Z WILL ASSERT WHEN ALU OUT IS ALL ONES.
- USE IS COMP SUB (1111) FOR COMPARE FOLLOWED BY BRANCH ON Z.
- MOV INSTRUCTION ALWAYS CLOCK Z.
- BRANCH CONDITION  $\emptyset$  (CHANGE FIELD) UNCONDITIONALLY BRANCHES ANYWHERE IN THE XK INSTRUCTION MEMORY. THE ADDRESS IS MADE UP FROM THE PCR AND THE ALU. BRANCH CONDITIONS 1-7, BRANCHES WITHIN THE FIELD. THE PAGE BITS ARE DEFINED BY BITS 11 AND 12 OF THE INSTRUCTION. THE ADDRESS ON THE PAGE IS DEFINED BY THE ALU OUT.
- ANY PAGE OR FIELD CAN BE INCREMENTED INTO.
- CSR BYTE ONE IS LOCKED OUT EXCEPT FOR MCLR WHEN SWITCH IS OFF.

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
JMP (SRC) BRANCH ADRS 9,8 COND															
100 (I) I 7-0															
110 (MEM) 111 (BRG)															
COND DEST															
000 CHANGE FIELD															
001 ALWAYS															
010 C															
011 Z															
100 BRG0															
101 BRG1															
110 BRG4															
111 BRG7															
SP															
SP, BRG															
MOV (SRC) MAR 01 LD HI 02 LD LO 01 INC DEST															
000 (I)															
001 (IBUS)															
101 (IBUS*)															
010 (MEM)															
011 (BRG)															
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C  
ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 1000  
SEL B (B) 1001  
A OR B (A+B) 1010  
A AND B (AB) 1011  
A OR B (A+B) 1100  
A XOR B A  $\oplus$  B 1101  
MOV INST CLOCK C

ALU f CODE  
25 COMP SUB (A-B) 1110  
IS COMP SUB (A-B-1) 1111  
ADD (A,B) 0000  
ADD W/C (A,B,C) 0001  
SUB W/C (A-B-C) 0010  
INC A (A+1) 0111  
A PLUS C (A,C) 0100  
2A (A,A) 0101  
2A W/C (A,A,C) 0110  
DECA (A-1) 0111  
SEL A (A) 10



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D

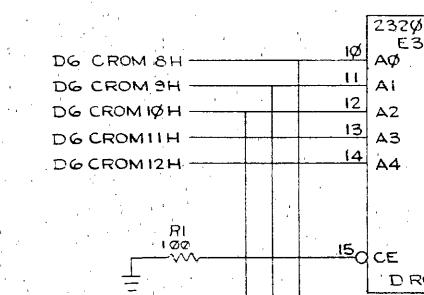
D

D8 BALU  $\phi$ (i)H

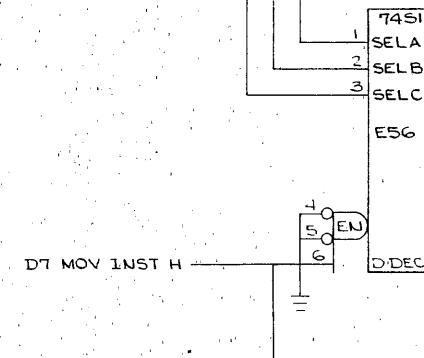
D8 BALU 1(i)H

D8 BALU 2(i)H

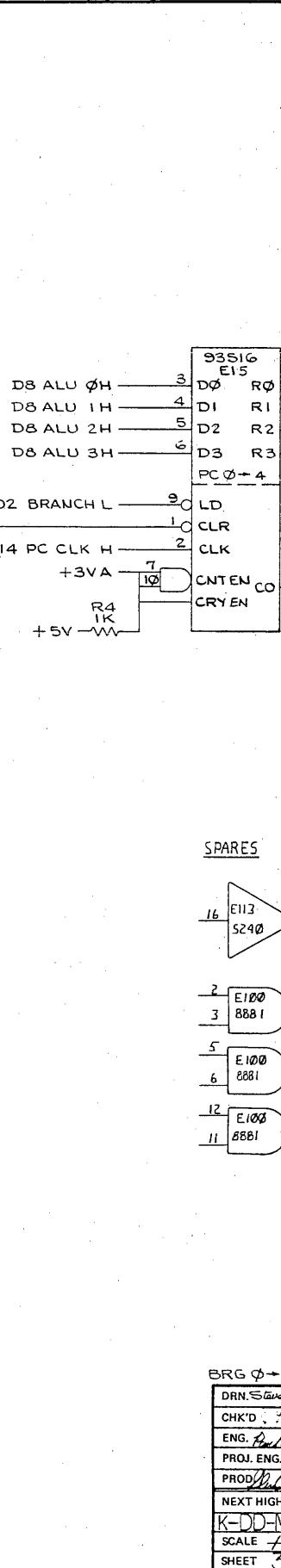
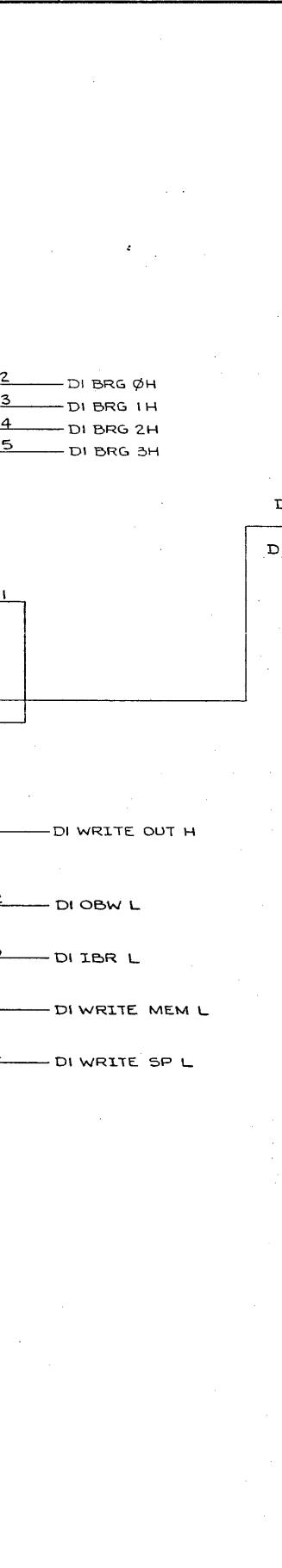
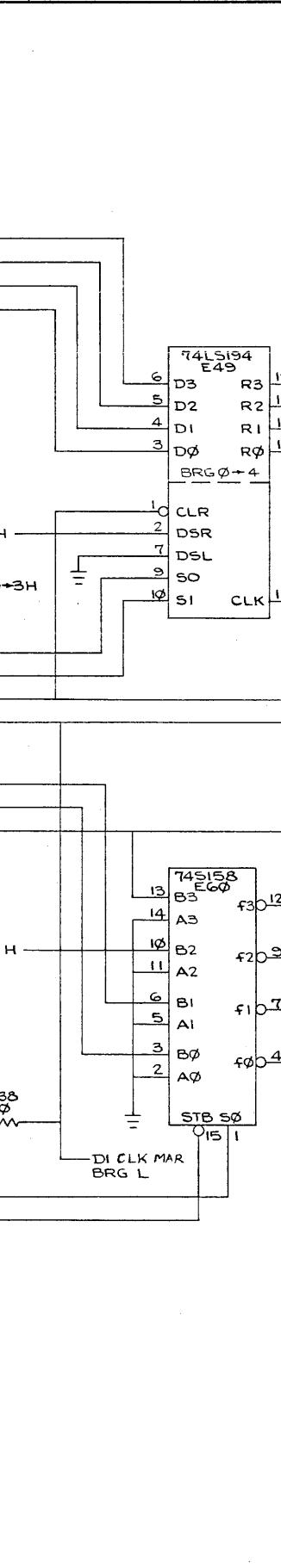
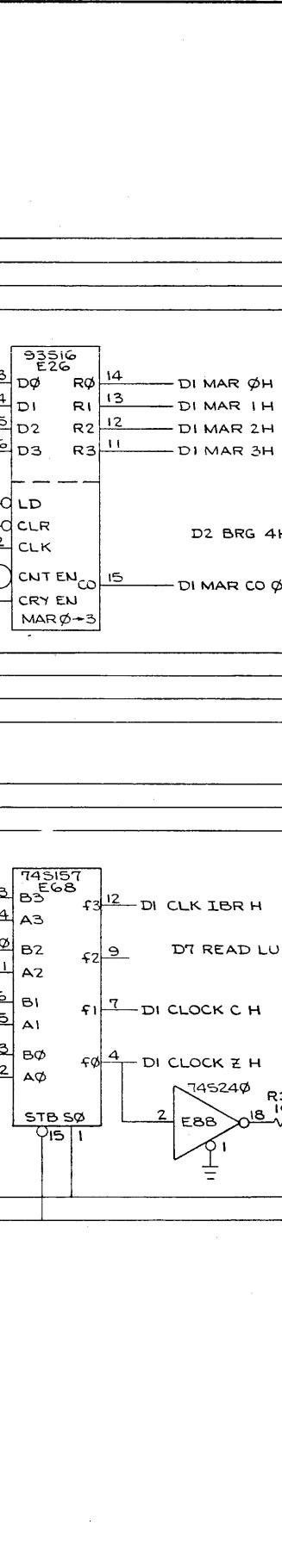
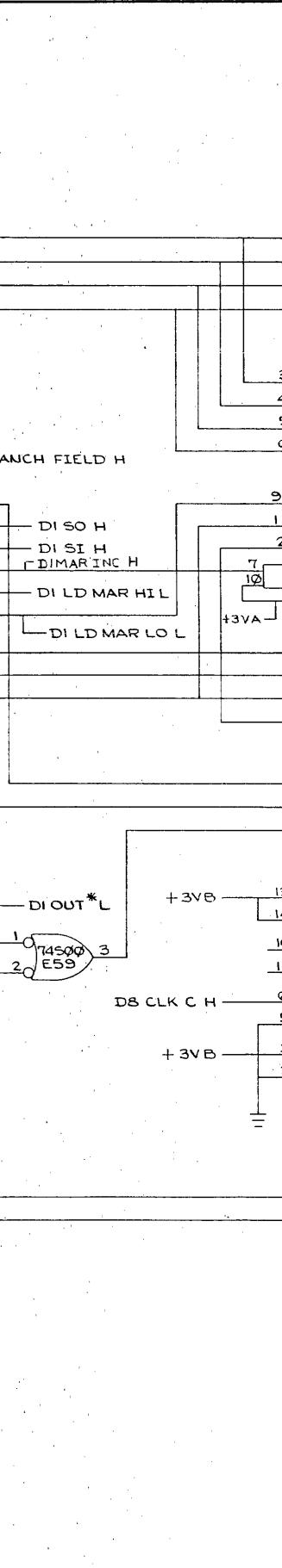
D8 BALU 3(i)H



DIG CLEAR L

D14 T12 $\phi$ L

REVISIONS  
CHANGE NO.  
REVISIONS  
CHANGE NO.  
REVISIONS  
CHANGE NO.  
REVISIONS  
CHANGE NO.



BRG  $\phi$ →3, MAR  $\phi$ →3, PC  $\phi$ →3, DROM, REG CLK  
 DRN. Steve Garity 7/3/78 FIRST USED ON  
 CHK'D. [Signature] 7/3/78 DMP II-AD digital  
 ENG. [Signature] TITLE DMP II  
 PROJ. ENG. [Signature] MICROCONTROLLER  
 PROD. [Signature] (D1)  
 NEXT HIGHER ASSY.  
 K-DD-M8207-0 SIZE CODE DCS NUMBER M8207-0-1 REV. R  
 SCALE 1 DIST. 3 OF 21 DIST. 1  
 SHEET 3 DIST. 1 DIST. 1

REV. R  
NUMBER M8207-0-1  
SIZE CODE DCS  
SHEET 3 DIST. 1 DIST. 1

A

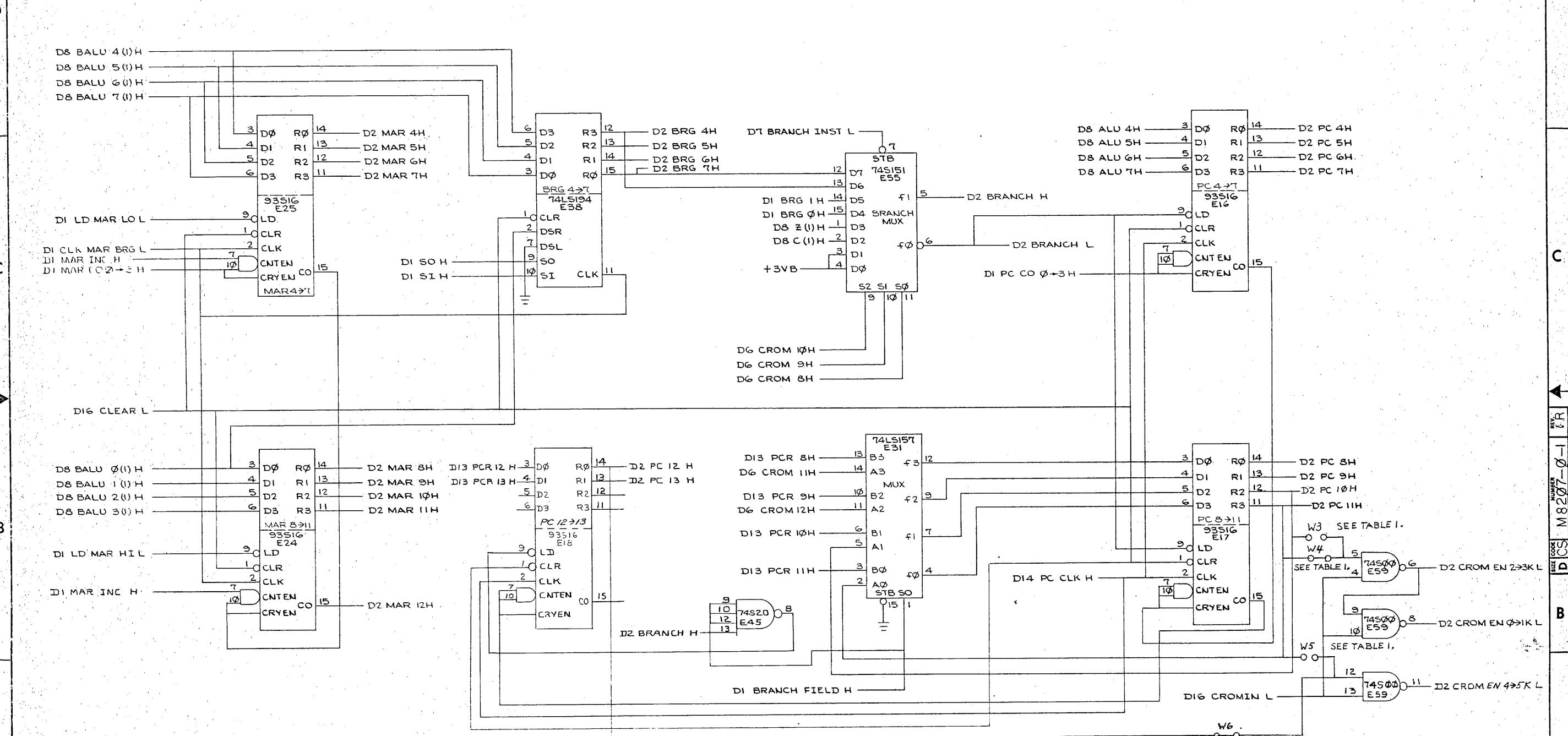
B

C

D

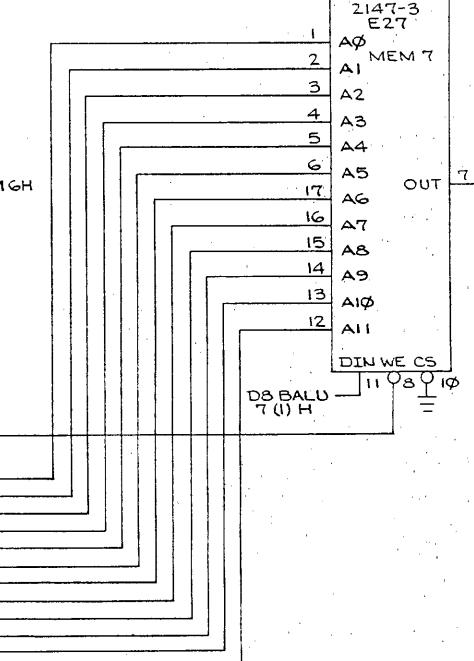
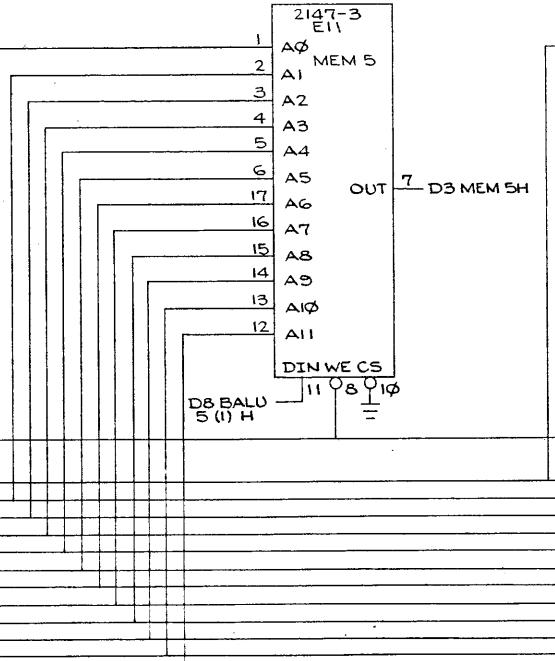
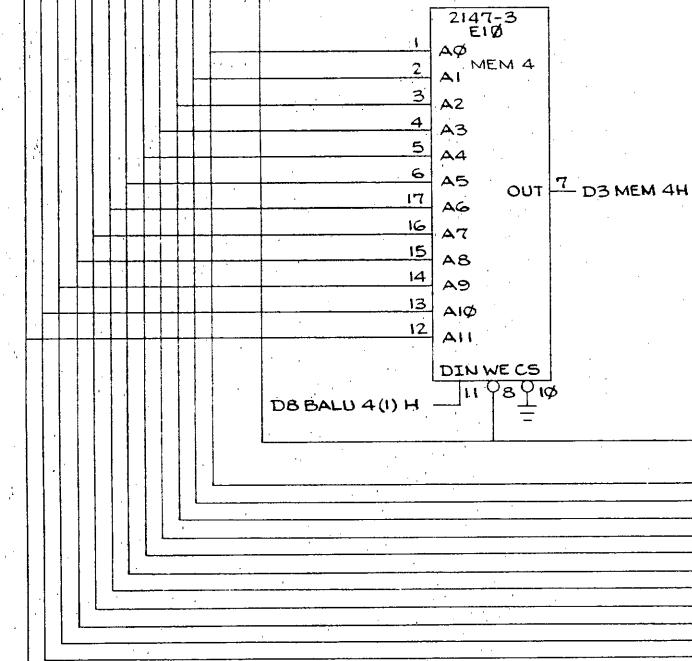
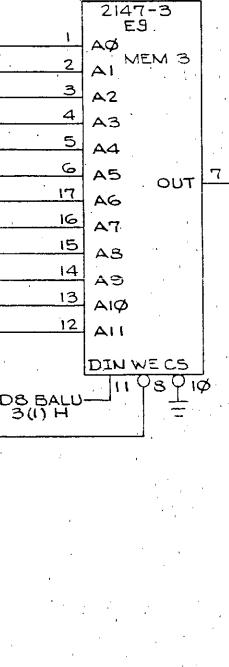
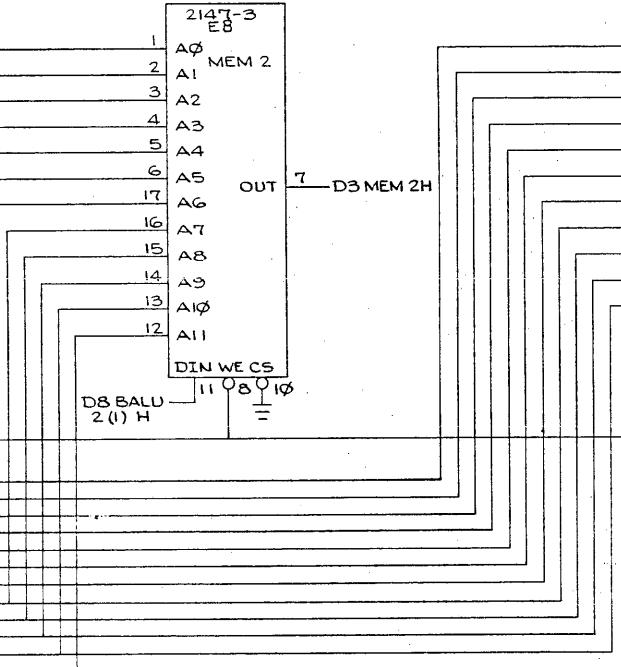
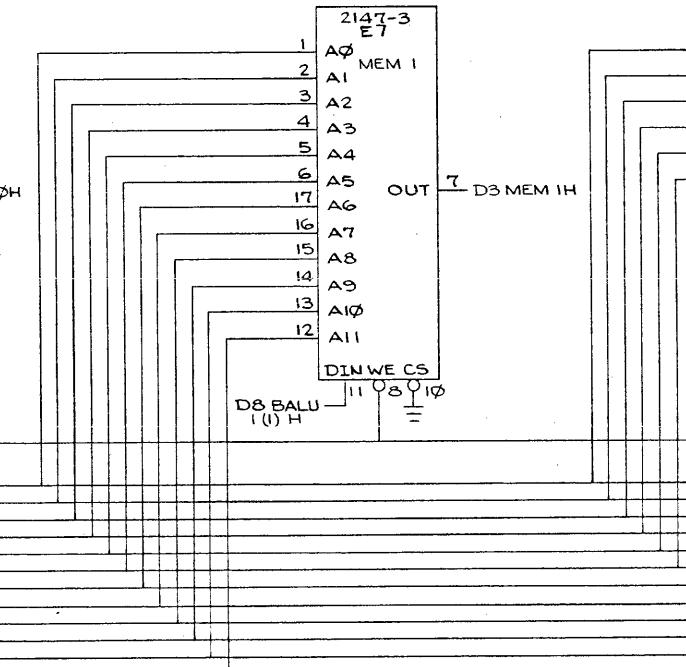
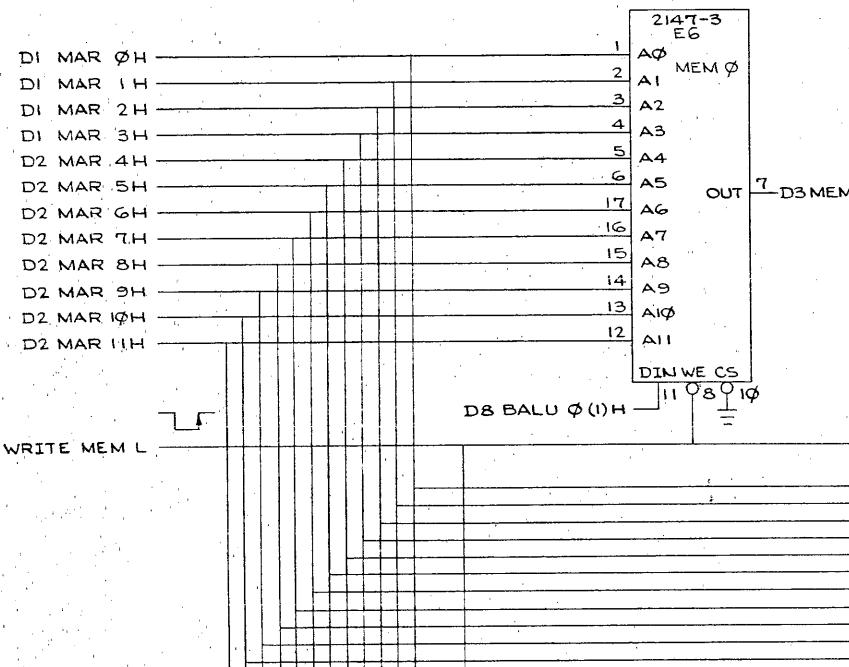
D

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BRG 4-7, MAR 4-11, PC4-13				
DRN. Steve Garity	7/1986			
FIRST USED ON				
CHK'D.	1/18/86 N78			
ENG.	Paul D. <i>Garity</i>			
PROJ. ENG.	1/18/86 <i>Garity</i>			
PROD.	1/18/86 <i>Garity</i>			
TITLE				
DMP1I				
MICROCONTROLLER				
(D2)				
NEXT HIGHER ASSY.				
K-DD-M8207-0				
SCALE	1/1			
SHEET	4 OF 21			
SIZE	CODE	NUMBER		REV.
D	CS	M8207-0-1		IR
DIST.				

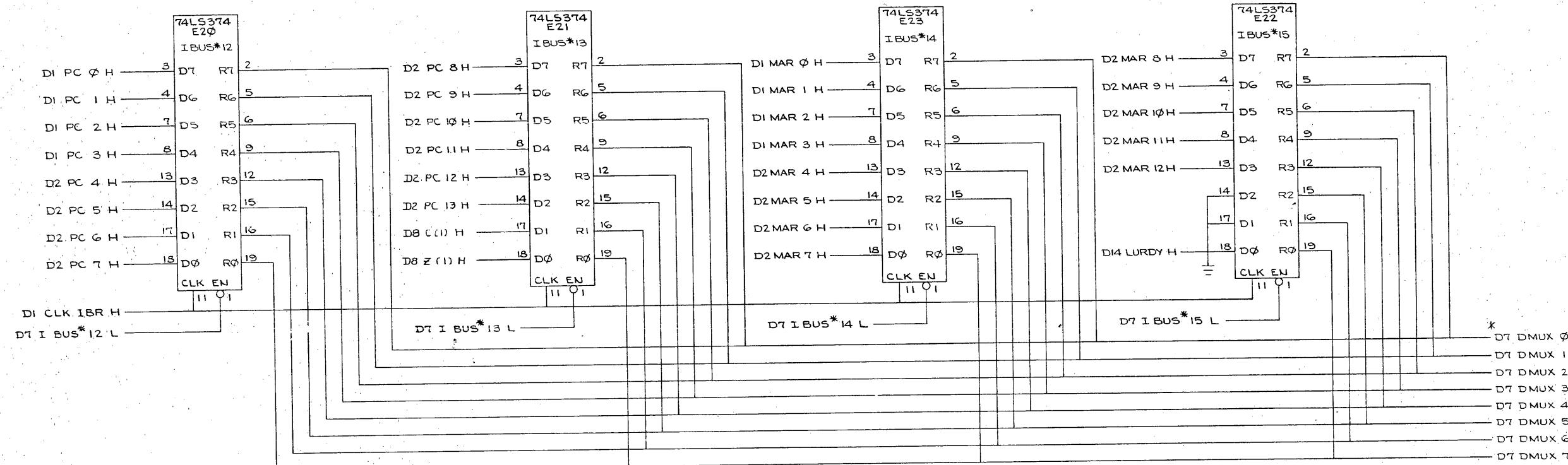
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MAIN MEMORY Ø-7		FIRST USED ON	
DRN. S/N	Serial No.	Issue No.	DMPII-AD digital
CHK'D	Initials	Ins. No.	
ENG.	Initials	Check by	
PROJ. ENG.	Initials	Marked by	
PROD.	Initials	Marked by	
NEXT HIGHER ASSY.			
K-DD-M8207-0		SIZE	CODE
SCALE 1		D	CS
SHEET 5 OF 21		NUMBER	M8207-0-1
		REV.	R

REVISIONS	CHANGE NO.	REV.
CHK		
MANUFACTURED BY		

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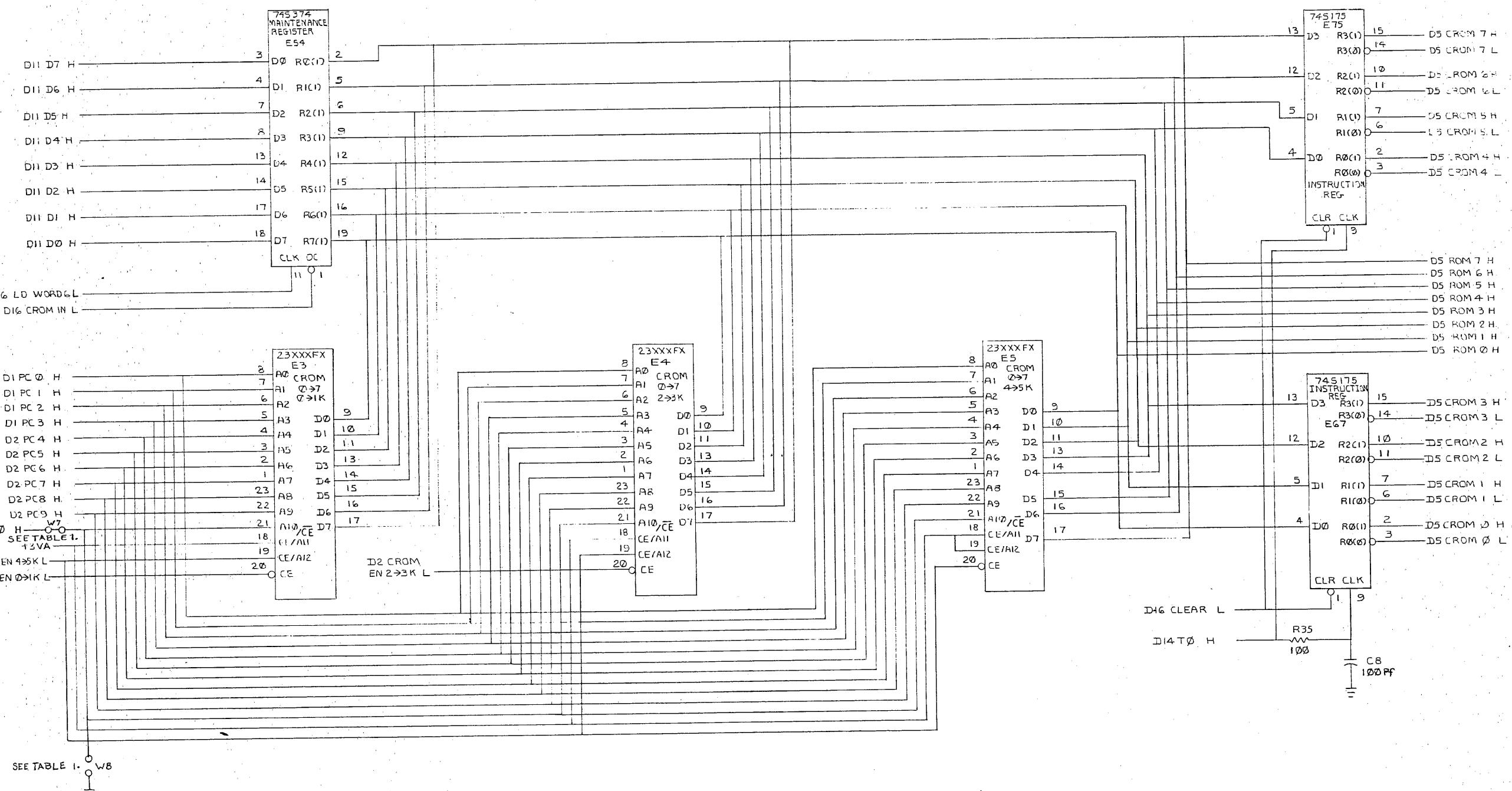


\* DMUX SIGNALS ARE TRI STATE

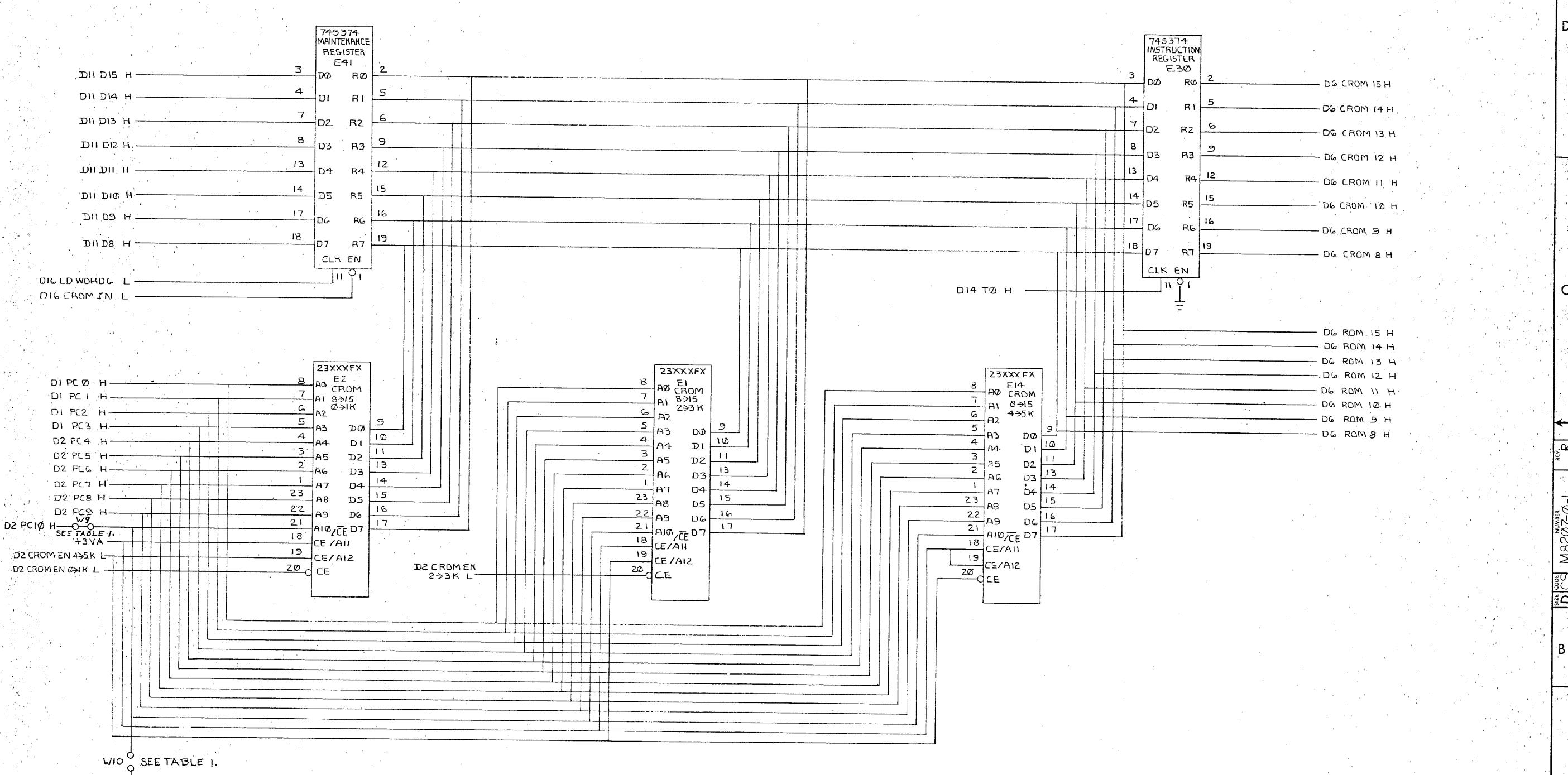
I BUS* REGISTER 12-15				
DRN. Steve Gandy	TYPE	FIRST USED ON		
CHK'D	7/19/78	DMPII-AD digital		
ENG.	Bob D. Allen	TITLE		
PROJ. ENG.	Bob D. Allen	DMPII		
PROD.	Bob D. Allen	MICROCONTROLLER		
(D4)				
NEXT HIGHER ASSY.				
K-DD-M8207-0				
SCALE	1	SIZE	CODE	NUMBER
SHEET	6	6	CS	M8207-0-1
DIST.	OF 21	REV.	R	

REV.
CHANGE NO.
CHK

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REVISEMENTS

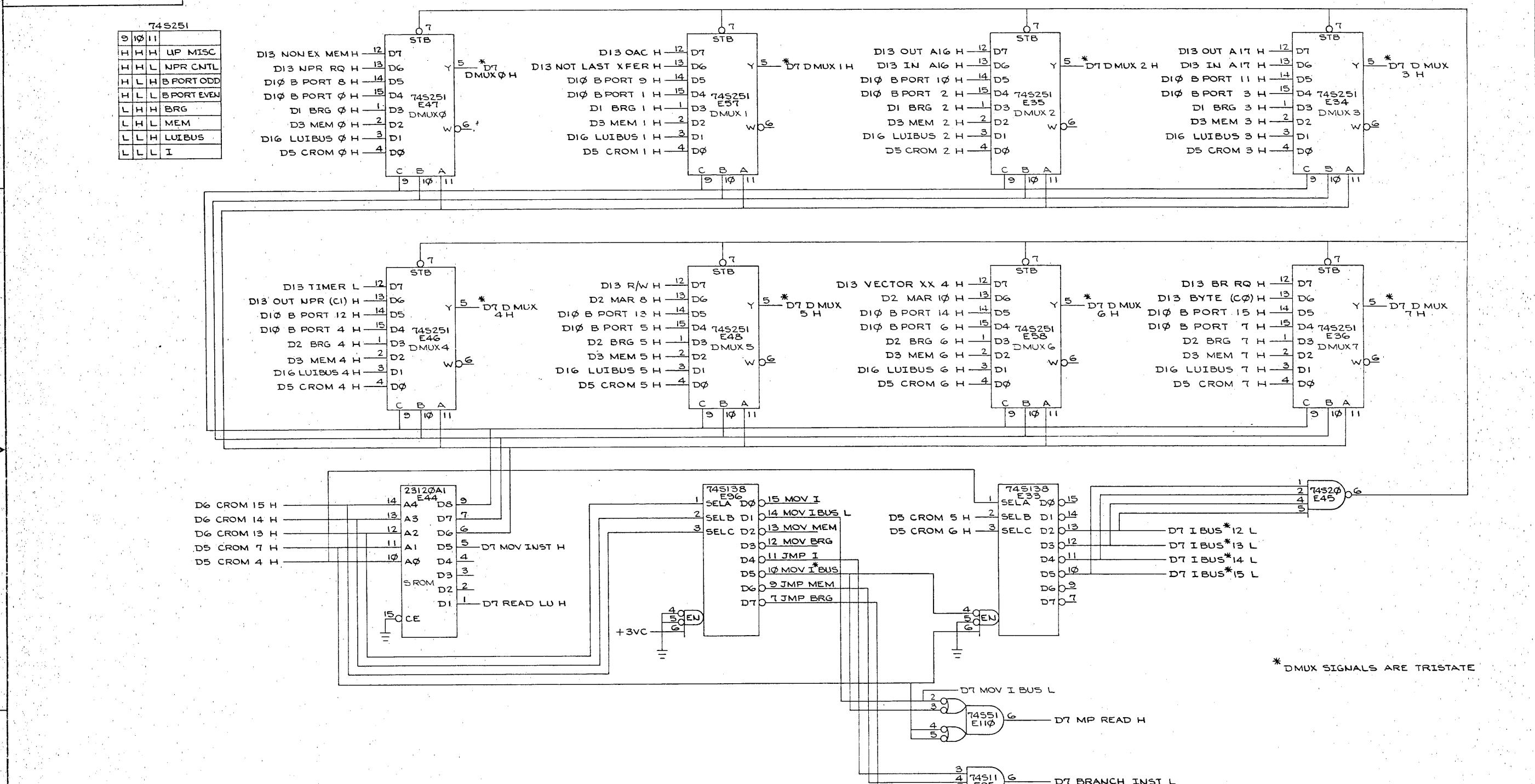
CHK. CHANGE NO. REV.

(CROM+IR+MAINTENANCE 8-15)

TITLE DMP11 (DG) SIZE CODE NUMBER DCS M8207-0-1 REV. R  
MICROCONTROLLER  
SCALE + + + SHEET 8 OF 21 DIST. 1  
DCC FORM NO. 002-124

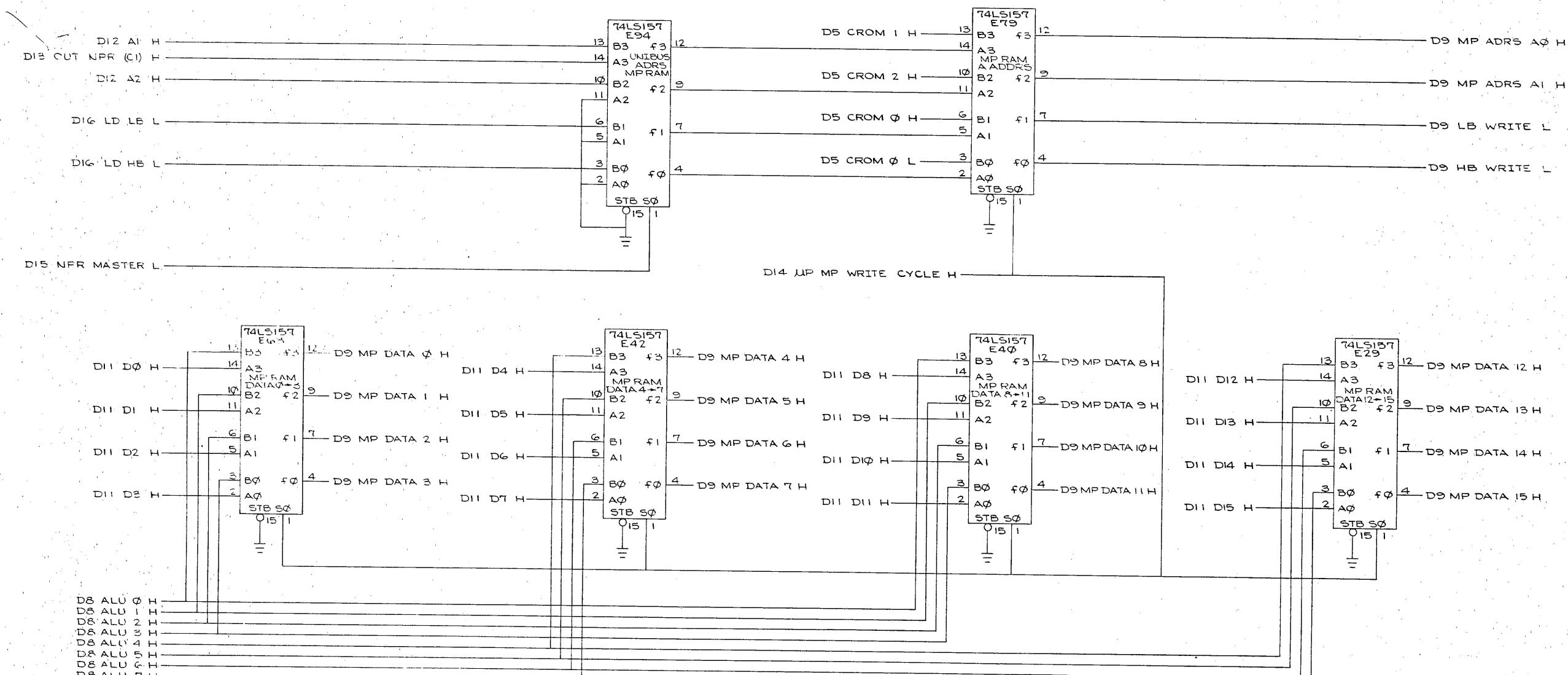
SIZE CODE DCS M8207-0-1 REV. R  
NUMBER DCS M8207-0-1 REV. R  
REV. R

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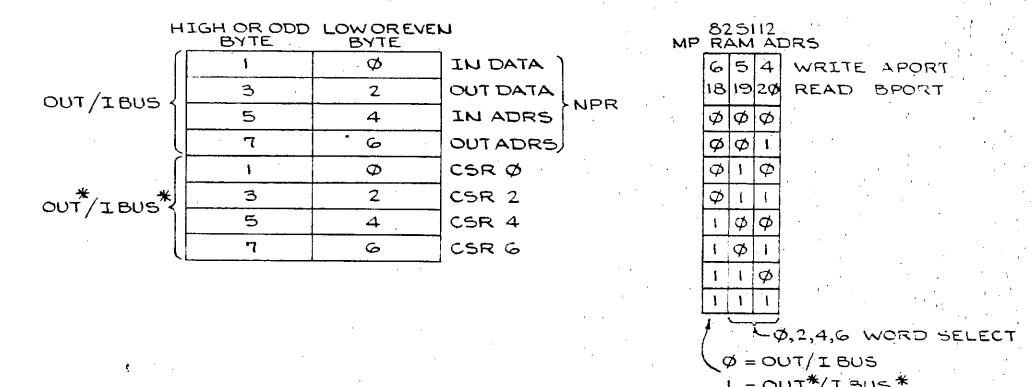
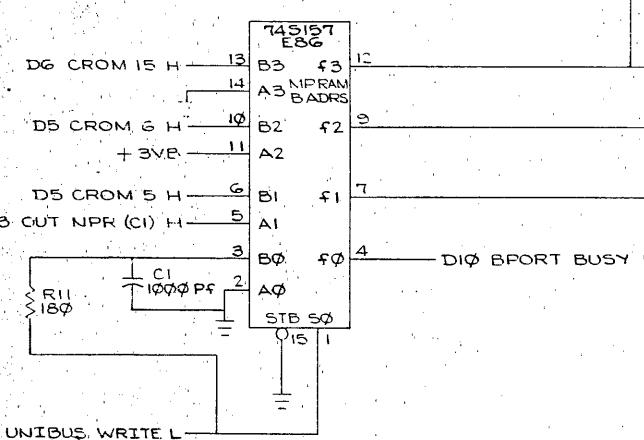
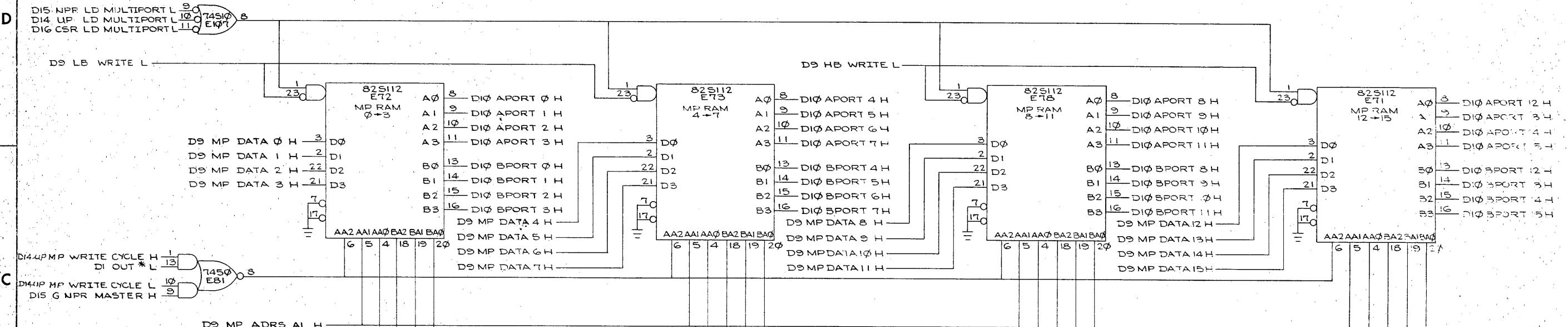
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MP RAM ADRS + DATA				
DRN. <i>Steve Garret</i>	10 JUNE 78	FIRST USED ON	DMP II-AD	digital
CHK'D		TITLE	DMP II	
ENG. <i>Paul D'Amico</i>	<i>Engineering</i>	MICROCONTROLLER		
PROJ. ENG. <i>Paul D'Amico</i>		(D9)		
PROD. <i>John C. Ladd</i>				
NEXT HIGHER ASSY.				
K-DD-M8207-0		SIZE	CODE	NUMBER
SCALE	/	D	CS	M8207-0-1
SHEET	OF 2	REV. R		
DIST.				

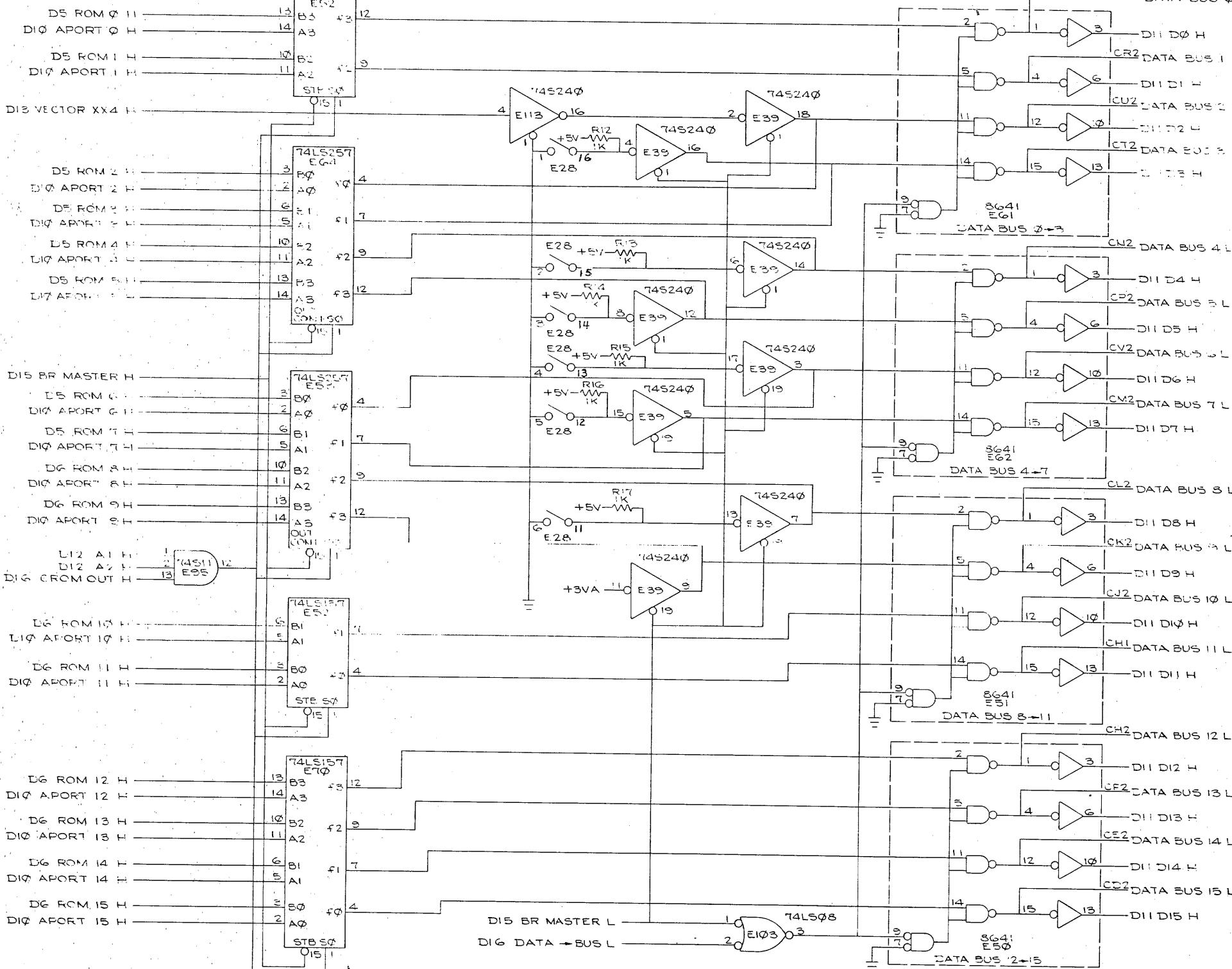
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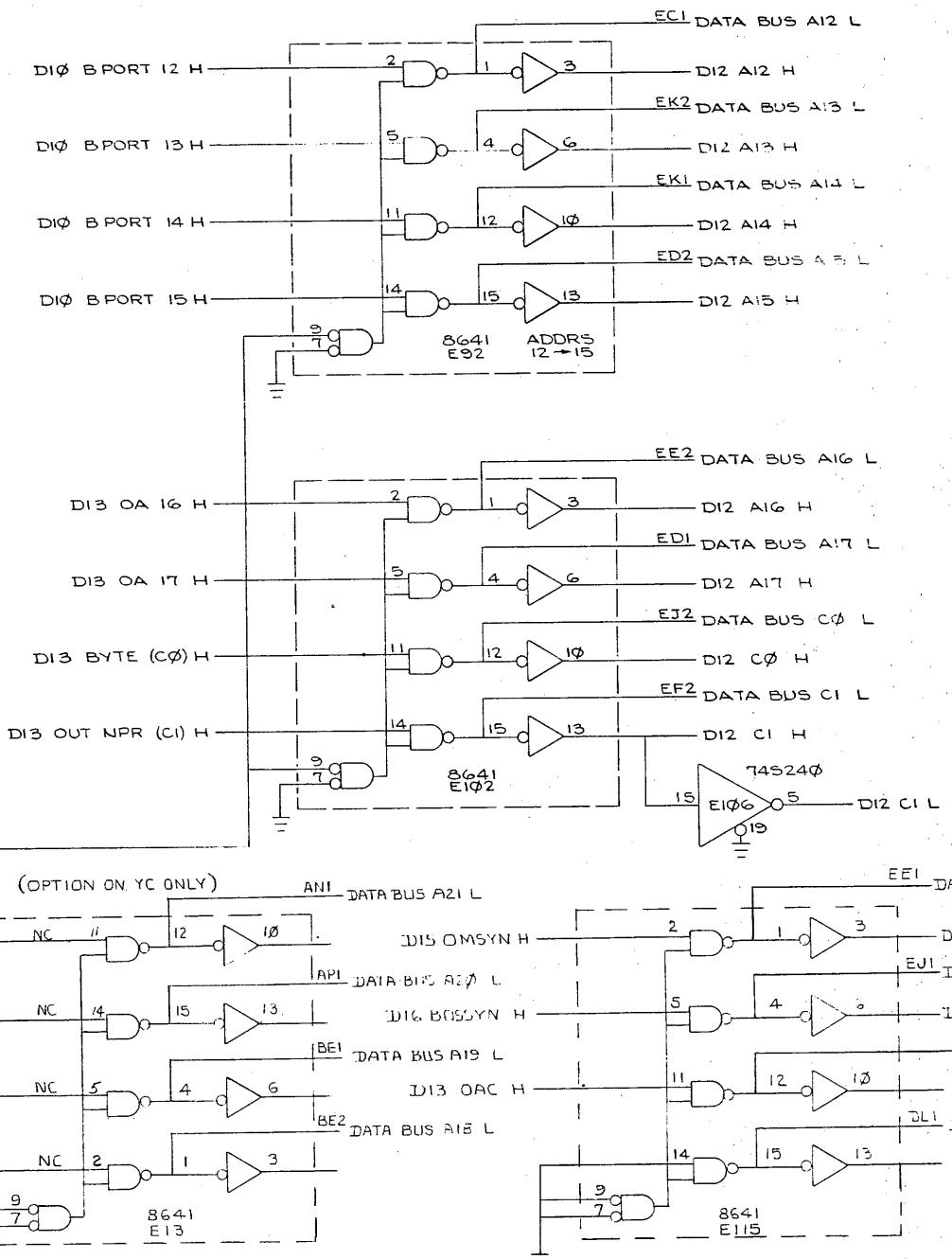
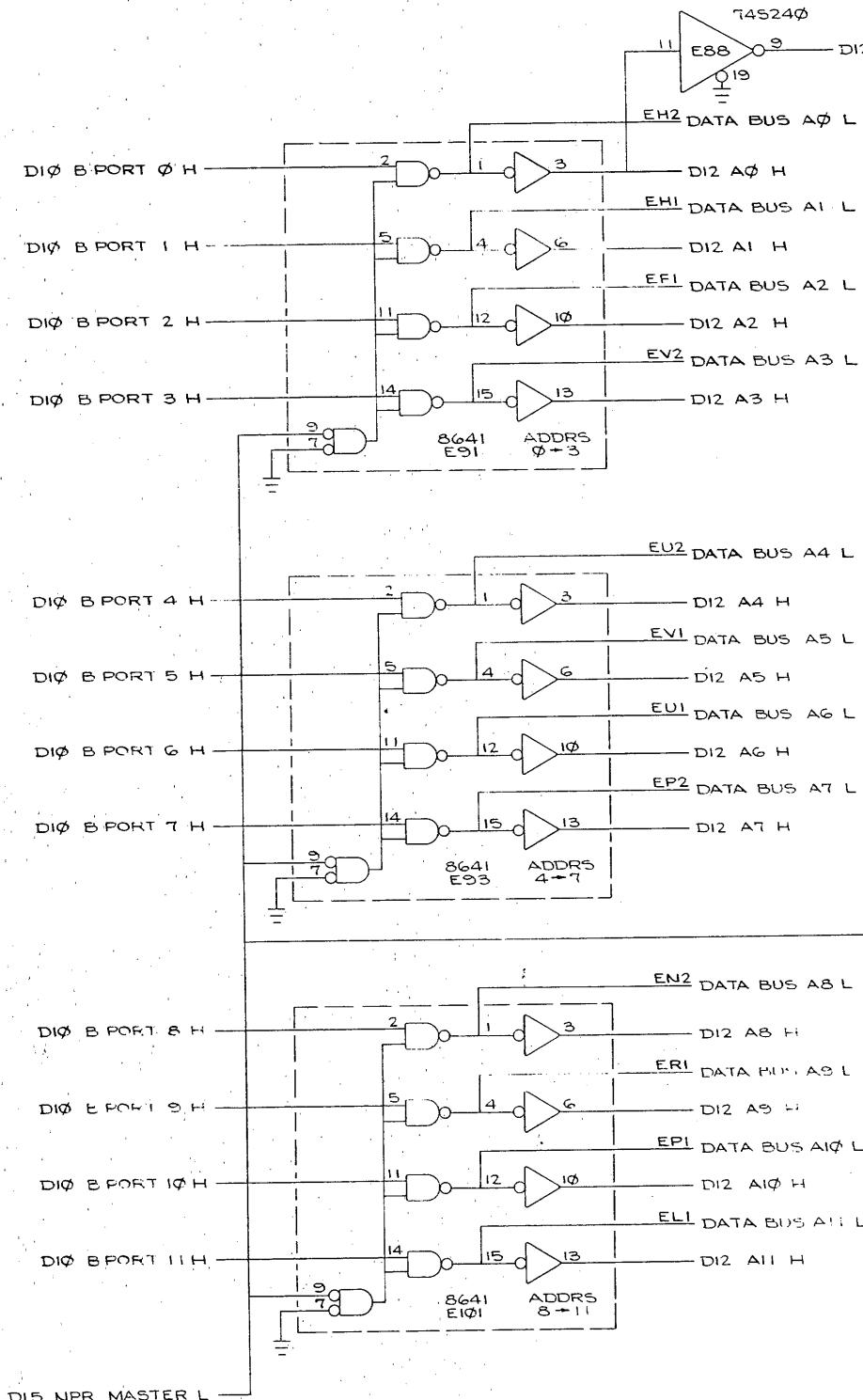
DRN/Steve Garity	1/June/78	FIRST USED ON	DMP II-AD digital	
CHK'D	1/1/78	REV.	TITLE	
ENG.	Bob Williams	1/1/78	DMP II	
PROJ. ENG.	Bob Williams	1/1/78	MICROCONTROLLER	
PROD.	Bob Williams	1/1/78	(D10)	
NEXT HIGHER ASSY.				
K-DD-M8207-0				
SCALE	1	SIZE	CODE	NUMBER
CHK	1	D	CS	M8207-0-1
REV.				
SHEET 12 OF 21 DIST.				

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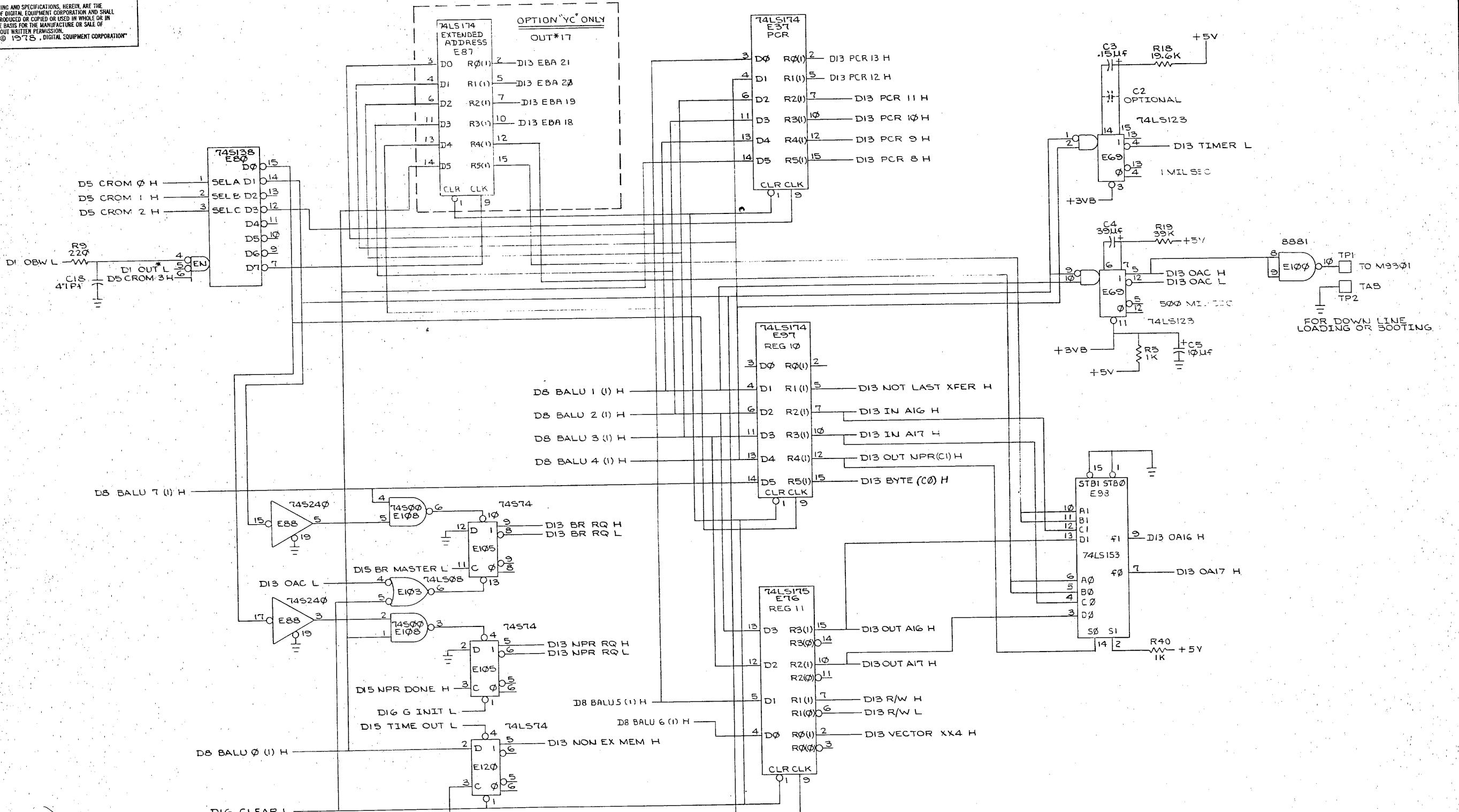
UNIBUS DATA INTERFACE		FIRST USED ON	
DRN. Steve Carrity	7/June/78	DMP II ADD digital	
CHK'D 1/10/78	9/June/78	TITLE DMP II	
ENG. Paul J. Labeau	7/June/78	MICROCONTROLLER	
PROJ. ENG. John J. Pomeroy	7/June/78	(D11)	
PROD. M. C. (John) Pomeroy	7/June/78		
NEXT HIGHER ASSY.			
K-DD-M8207-0		SIZE D	CODE CS
SCALE 1/4		NUMBER M8207-0-1	REV. R
REVISIONS		SHEET 13 OF 21	
CHANGE NO.		DIST.	
CHK			

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UNIBUS ADDS INTERFACE		FIRST USED ON	TITLE
DRN. Steve Canary	Rev. B		DMP II-HD digital
CHK'D	Jan 1978		
ENG.	John [unclear]		
PROJ. ENG.	John [unclear]		
PROD.	John [unclear]		
NEXT HIGHER ASSY.			
K-DO-M8207-0	SIZE	CODE	NUMBER
SCALE 1	D	CS	M8207-0-1
SHEET 14 OF 21			REV. R
DIST.			

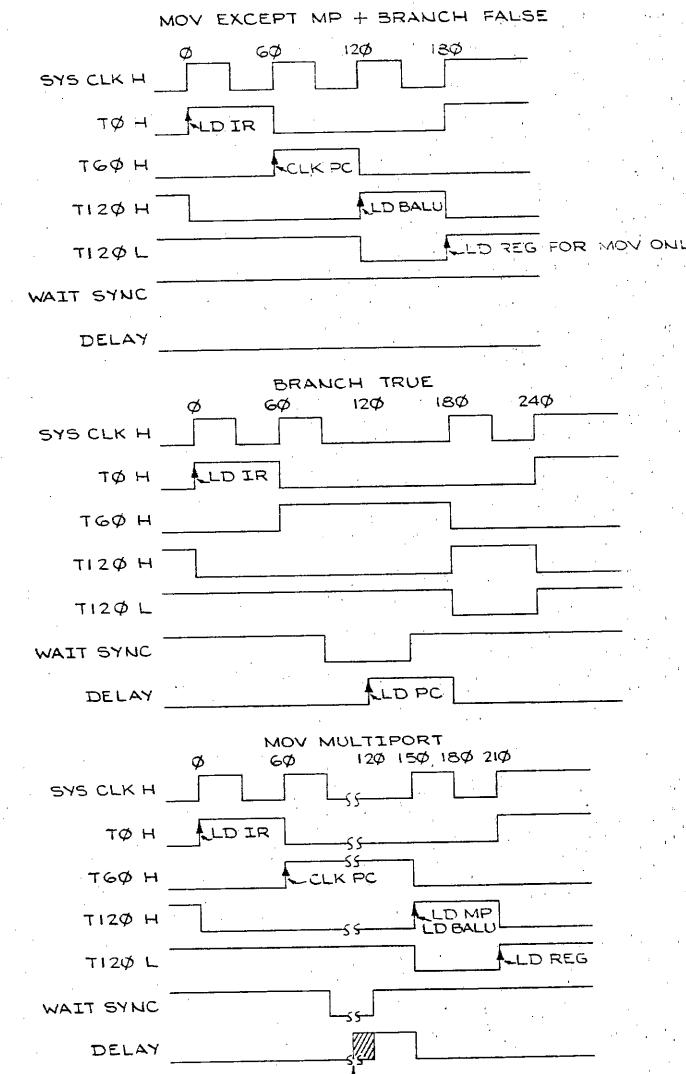
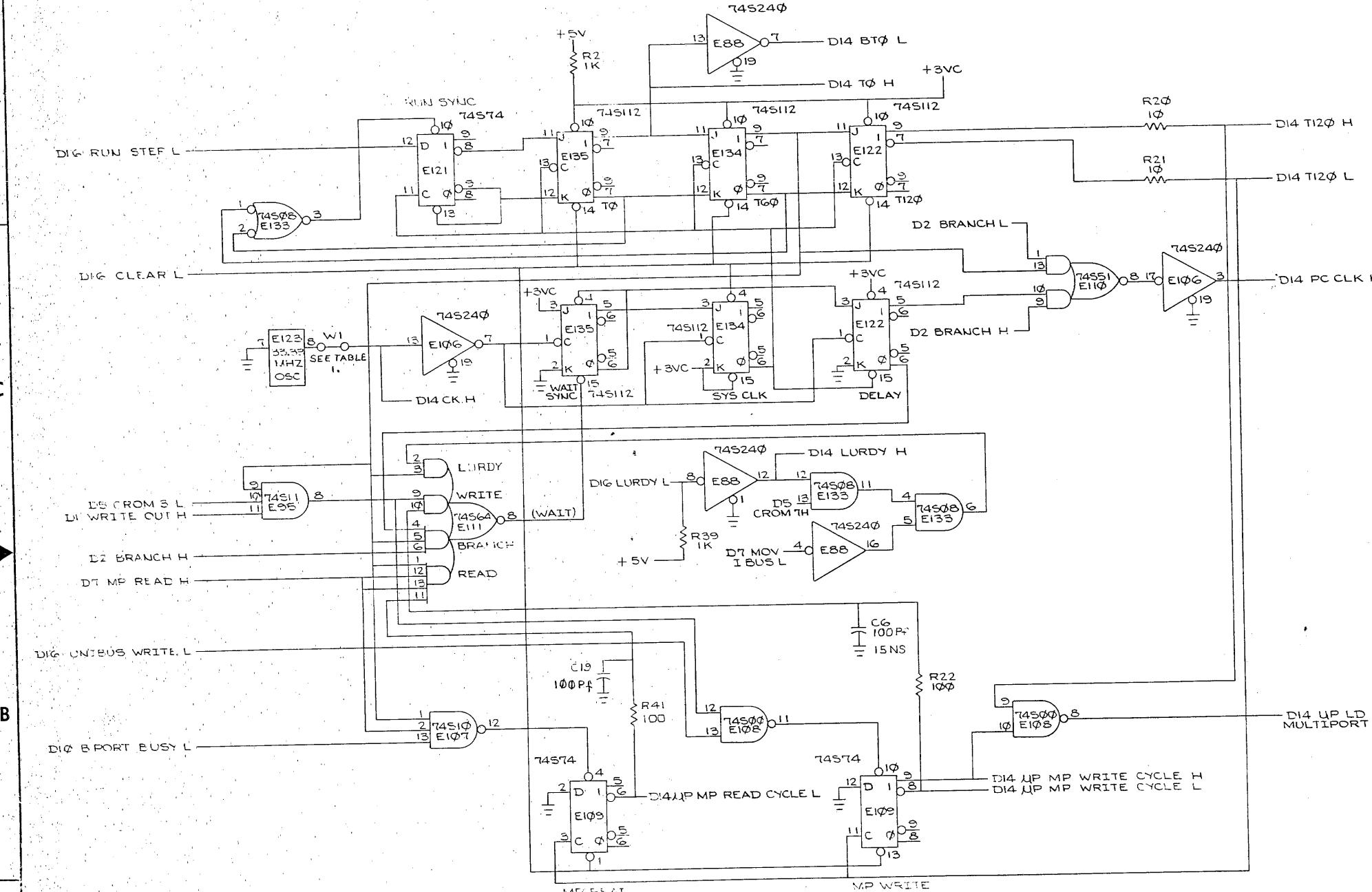
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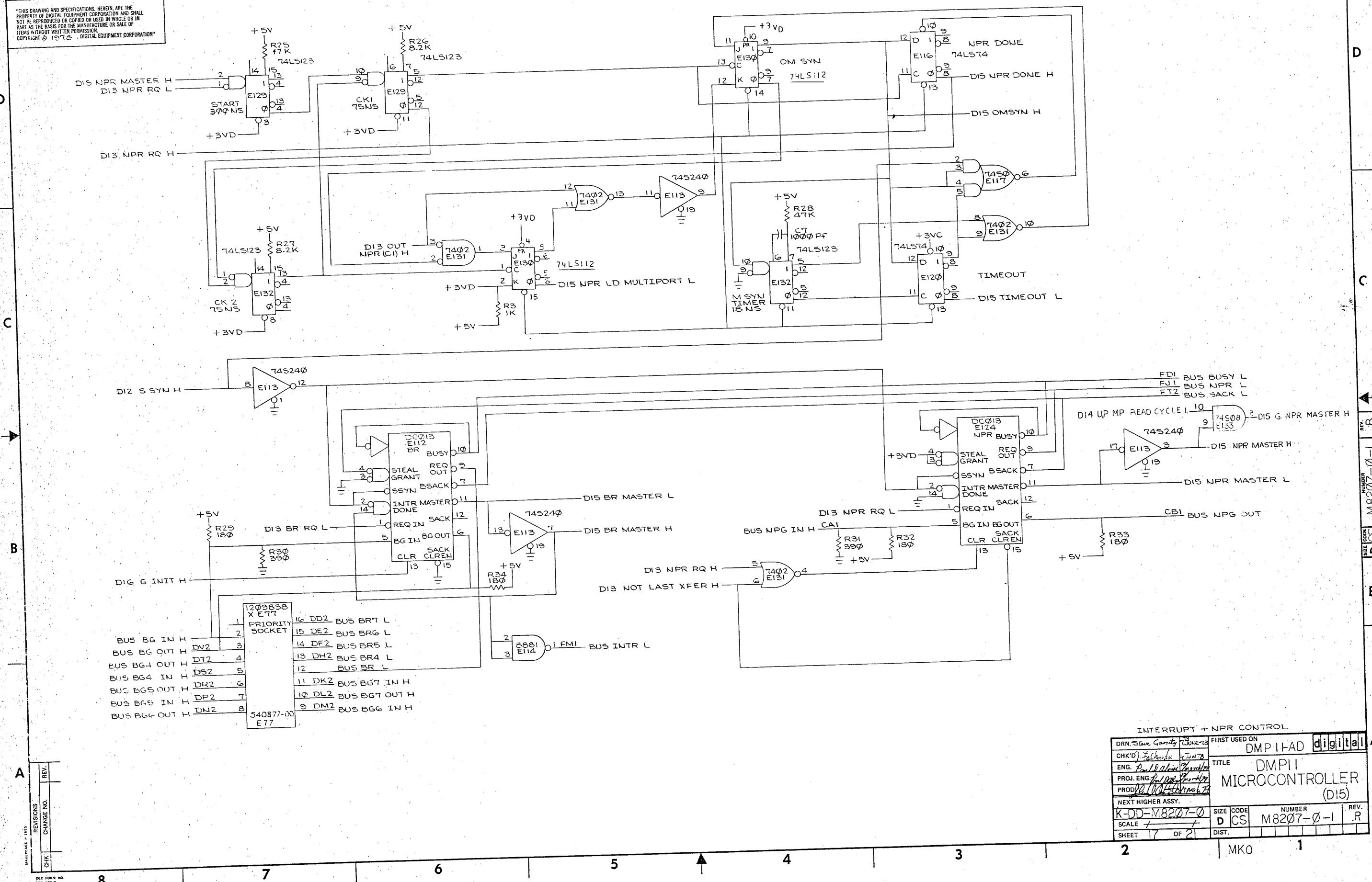


PROGRAMS COUNTER REGISTERS,  
NPR CONTROL, MP MISC REGISTER

DRN. <i>Steve Garity</i>	15 JUNE 78	FIRST USED ON	DMP 11-AD	digital
CHK'D <i>John</i>	5/20/78	TITLE		
ENG. <i>Bill</i>	<i>Patricia</i>	DMP II		
PROJ. ENG. <i>Paul</i>	<i>Tommy</i>	MICROCONTROLLER		
PROD. <i>John</i>	<i>Mark</i>	(D13)		
NEXT HIGHER ASSY.				
K-DD-M8207-0		SIZE	CODE	NUMBER
SCALE	1	D	CS	M8207-0-1
SHEET	15	OF	2	REV. R
DIST.				

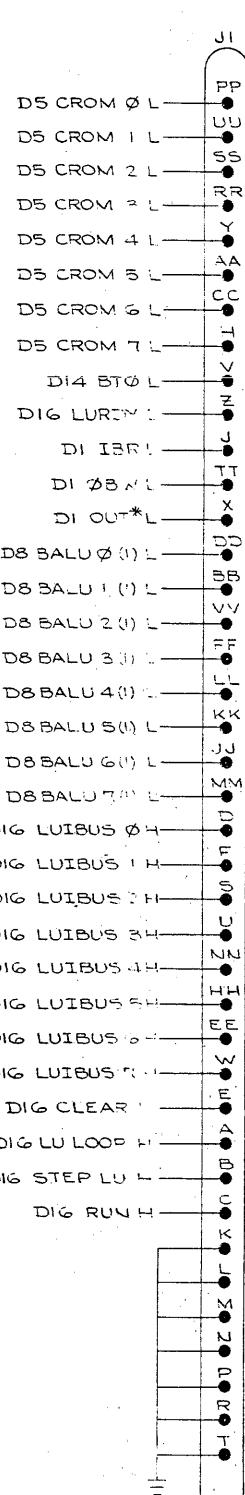
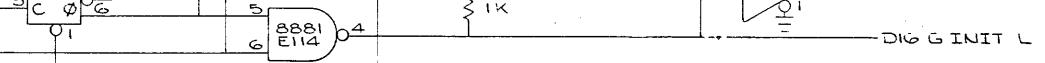
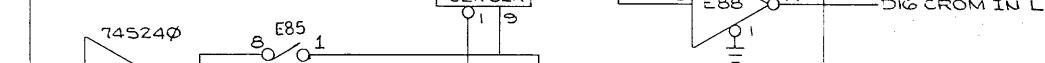
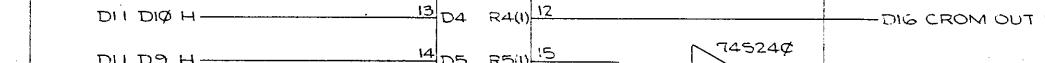
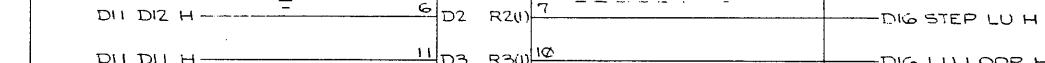
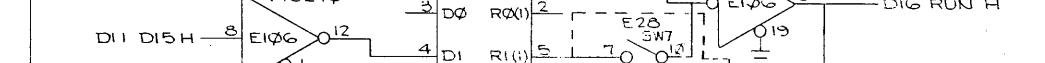
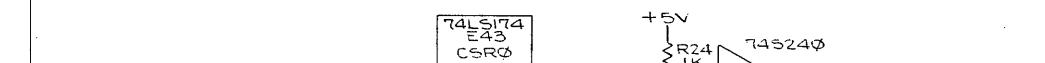
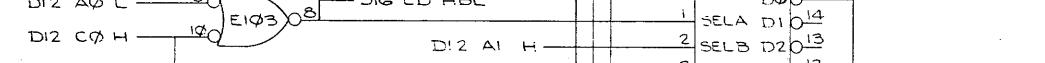
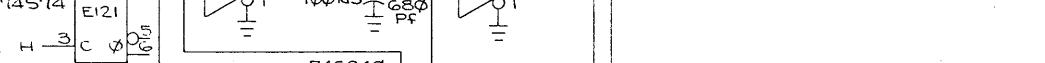
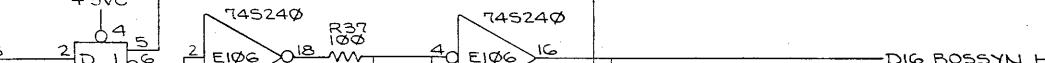
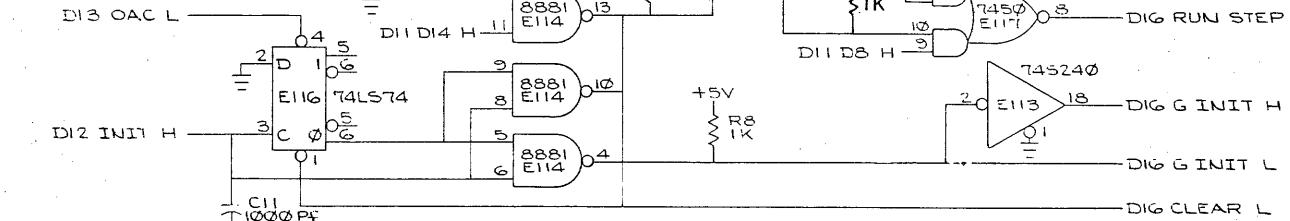
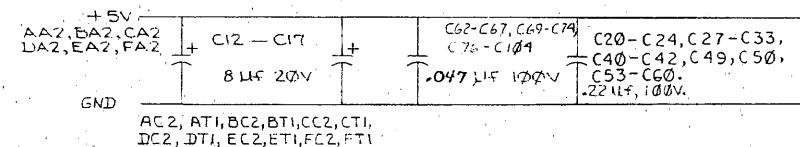
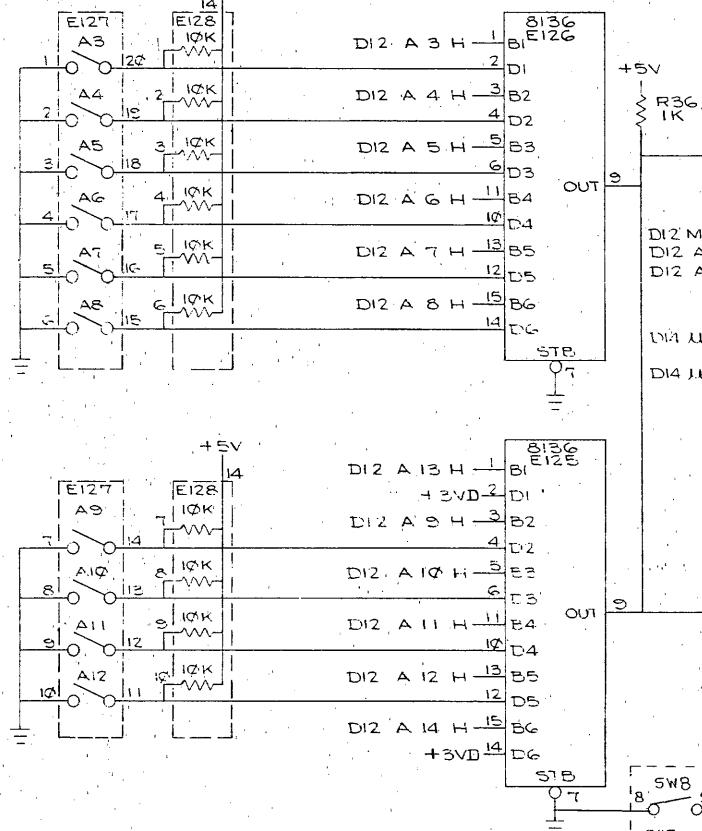
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INTERRUPT + NMR CONTROL			
DRN. Steve Garry	7/16/84		
CHK'D	7/16/84		
ENG. Paul D. Abbot	7/16/84		
PROJ. ENG. Paul D. Abbot	7/16/84		
PROD. D. (D) 7/16/84	7/16/84		
NEXT HIGHER ASSY.			
K-DD-Y8207-0			
SCALE	1		
SHEET	1 OF 2		
FIRST USED ON			
DMP II AD			
digital			
TITLE			
DMP II			
MICROCONTROLLER			
(D15)			
SIZE	CODE	NUMBER	REV.
D	CS	M8207-0-1	R
DIST.			

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UNIBUS CSR CONTROL CSRQ, I/O PORT	
DRN	State
State	Capacity
CHK'D	7.1.178
ENG.	Build
PROJ. ENG.	Build
PROD.	Build
NEXT HIGHER ASSY.	
K-00-M8207-0	REV.
SCALE	
SIZE	CODE
D	CS
M8207-0-1	REV.
SHEET	18
OF	2
DIST.	

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	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	BER
D3 MEM $\phi$ H		1		1													
MEM 1 H			1														
MEM 2 H			1														
MEM 3 H			1														
MEM 4 H			1														
MEM 5 H			1														
MEM 6 H			1														
MEM 7 H			1														
D5 CROM $\phi$ H			1	1	1	1											
CROM $\phi$ L			1		1										1	PP	
CROM 1 H			1	1	1	1											
CROM 1 L			1												1	UU	
CROM 2 H			1	1	1	1											
CROM 2 L			1												1	SS	
CROM 3 H			1	1	1												
CROM 3 L			1												1	RR	
CROM 4 H			1	3	1											1	Y
CROM 4 L			1														
CROM 5 H			1	2	1	1											
CROM 5 L			1													1	AA
CROM 6 H			1	2	1	1											
CROM 6 L			1												1	CC	
CROM 7 H			1	5	1												
CROM 7 L			1												1	H	
INES) ROM $\phi$ - 7 H			4														
D6 CROM 8 H	2	1		1													
CROM 9 H	2	1		1													
CROM 10 H	2	1		1													
CROM 11 H	1	1		1													
CROM 12 H	1	1		1													
CROM 13 H				1	2												
CROM 14 H				1	2	1											
CROM 15 H				1	2		1										
ES) ROM 8 - 15 H			4														
D7 DMUX $\phi$ H		4		1	1												
DMUX 1 H		4		1	1												
DMUX 2 H		4		1	1												
DMUX 3 H		4		1	1												
DMUX 4 H		4		1	1												
DMUX 5 H		4		1	1												
DMUX 6 H		4		1	1												
DMUX 7 H		4		1	1												
MOV INST H	3			1													
MP READ H				1												3	
BRANCH INST L		1			1												
READ LU H	1				1												
IBUS*12 L			1		2												
13 L			1		2												
14 L			1		2												
15 L			1		2												
MOVIBUS L				2											1		

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	BE
D8 ALU $\emptyset$ H	1							2	2								
ALU 1 H	1							2	2								
ALU 2 H	1							2	2								
ALU 3 H	1							2	2								
ALU 4 H	1							2	2								
ALU 5 H	1							2	2								
ALU 6 H	1							2	2								
ALU 7 H	1							2	2								
BALU $\emptyset$ (I) H	2	2	1					1			4						
BALU $\emptyset$ (I) L								2								1 DD	
BALU 1 (I) H	2	1	1					1			4						
BALU 1 (I) L								2								1 BB	
BALU 2 (I) H	2	1	1					1			4						
BALU 2 (I) L								2								1 VV	
BALU 3 (I) H	2	1	1					1			4						
BALU 3 (I) L								2								1 FF	
BALU 4 (I) H	2	1						1			4						
BALU 4 (I) L								2								1 LL	
BALU 5 (I) H	2	1						1			3						
BALU 5 (I) L								2								1 KK	
BALU 6 (I) H	2	1						1			1						
BALU 6 (I) L								2								1 JJ	
BALU 7 (I) H	2	1						1			2						
BALU 7 (I) L								2								1 MM	
C (I) H	1	1						2									
CLK C H	1							1									
$\Sigma$ (I) H	1	1						1									
D9 HB WRITE L										1	2						
LB WRITE L										1	2						
MP ADRS A $\emptyset$ H										1	4						
MP ADRS A 1 H										1	4						
MP DATA $\emptyset$ H										1	1						
MP DATA 1 H										1	1						
MP DATA 2 H										1	1						
MP DATA 3 H										1	1						
MP DATA 4 H										1	1						
MP DATA 5 H										1	1						
MP DATA 6 H										1	1						
MP DATA 7 H										1	1						
MP DATA 8 H										1	1						
MP DATA 9 H										1	1						
MP DATA 10 H										1	1						
MP DATA 11 H										1	1						
MP DATA 12 H										1	1						
MP DATA 13 H										1	1						
MP DATA 14 H										1	1						
MP DATA 15 H										1	1						
DI $\emptyset$ APORT $\emptyset$ H										1	1						
APORT 1 H										1	1						
APORT 2 H										1	1						
APORT 3 H										1	1						
APORT 4 H										1	1						
APORT 5 H										1	1						
APORT 6 H										1	1						

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DI0 APORT 7 H																
APORT 8 H																
APORT 9 H																
APORT 10 H																
APORT 11 H																
APORT 12 H																
APORT 13 H																
APORT 14 H																
APORT 15 H																
BPORt 0 H																
BPORt 1 H																
BPORt 2 H																
BPORt 3 H																
BPORt 4 H																
BPORt 5 H																
BPORt 6 H																
BPORt 7 H																
BPORt 8 H																
BPORt 9 H																
BPORt 0 H																
BPORt 11 H																
BPORt 12 H																
BPORt 13 H																
BPORt 14 H																
BPORt 15 H																
BPORt BUSY L																
D11 D0 H																
D1 H																
D2 H																
D3 H																
D4 H																
D5 H																
D6 H																
D7 H																
D8 H																
D9 H																
DI0 H																
D11 H																
D12 H																
D13 H																
D14 H																
D15 H																
DATA BUS 0 L														2		CS
DATA BUS 1 L														2		CR
DATA BUS 2 L														2		CU
DATA BUS 3 L														2		CT
DATA BUS 4 L														2		CN
DATA BUS 5 L														2		CP
DATA BUS 6 L														2		CV
DATA BUS 7 L														2		CM
DATA BUS 8 L														2		CL
DATA BUS 9 L														2		CK
DATA BUS 10 L														2		CJ
DATA BUS 11 L														2		CH
DATA BUS 12 L														2		CH
DATA BUS 13 L														2		CF
DATA BUS 14 L														2		CE

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		DMP11		SIZE	CODE	NUMBER		REV.
MICROCONTROLLER		D	CS	M8207-Ø-1				R
SCALE	/	/	SHEET	19	OF	21	DIST.	

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	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	BERG
DIG CLEAR L	3	6		2		4					6	6	G	E		DIG	
CROM IN L		3		1	1	1								1			
CROM OUT H										1				1			
SR LD MULTIPORT L										1				1			
BOSSYN H											1			2			
DATA → BUS L											1			1			
G INIT L												1		3			
LD HB L										1				2			
LD LB L										1				1			
LD WORD G L				1	1	1								1			
LUIBUS Ø H					1									1	D		
LUIBUS 1 H						1								1	F		
LUIBUS 2 H							1							1	S		
LUIBUS 3 H								1						1	U		
LUIBUS 4 H									1					1	NN		
LUIBUS 5 H										1				1	HH		
LUIBUS 6 H											1			1	EE		
LUIBUS 7 H											1			1	W		
LU LOOP H														2	A		
RUN H														4	C		
RUN STEP L														1	1		
STEP LU H														2	B		
UNIBUS WRITE L										2		1		1			
G INIT H														1	1		
LURDY L														2	1	Z	

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	EUS
2 DATA BUS INIT L											2						DLI
DATA BUS A18 L											2						882
DATA BUS A19 L											2						BEI
DATA BUS A20 L											2						API
DATA BUS A21 L											2						ANI
3 EBA 18											1	1					
EBA 19											1	1					
EBA 20											1	1					
EBA 21											1	1					
TABLE EXTEND EUS														1	3		

BUS TIMING THRU BERG

The diagram illustrates the timing of various bus signals over five time intervals (T1 to T5). The signals are:

- CROM LINES φ-7 L**: Valid from T1 to T5.
- LINE UNIT IBUS φ-7 H**: Valid from T1 to T5.
- BALU LINES φ-7 L**: Valid from T1 to T5.
- CLOCK DATA TO LU (OUT BUS WRITE)**: Valid from T1 to T5.
- TIMING PULSE**: A pulse occurring between T1 and T2.
- BTφ L**: Valid from T1 to T5.
- DATA LUIBUS VALID**: Valid from T1 to T5.
- CROM LINES VALID ADRS IN/OUT**: Valid from T1 to T5.
- BALU VALID FROM PREVIOUS INSTRUCTION**: Valid from T1 to T5.
- IBR L**: Valid from T1 to T5.
- φBW L**: Valid from T1 to T5.

Timing markers: φ, 6φ, 12φ, 18φ, 21φ.

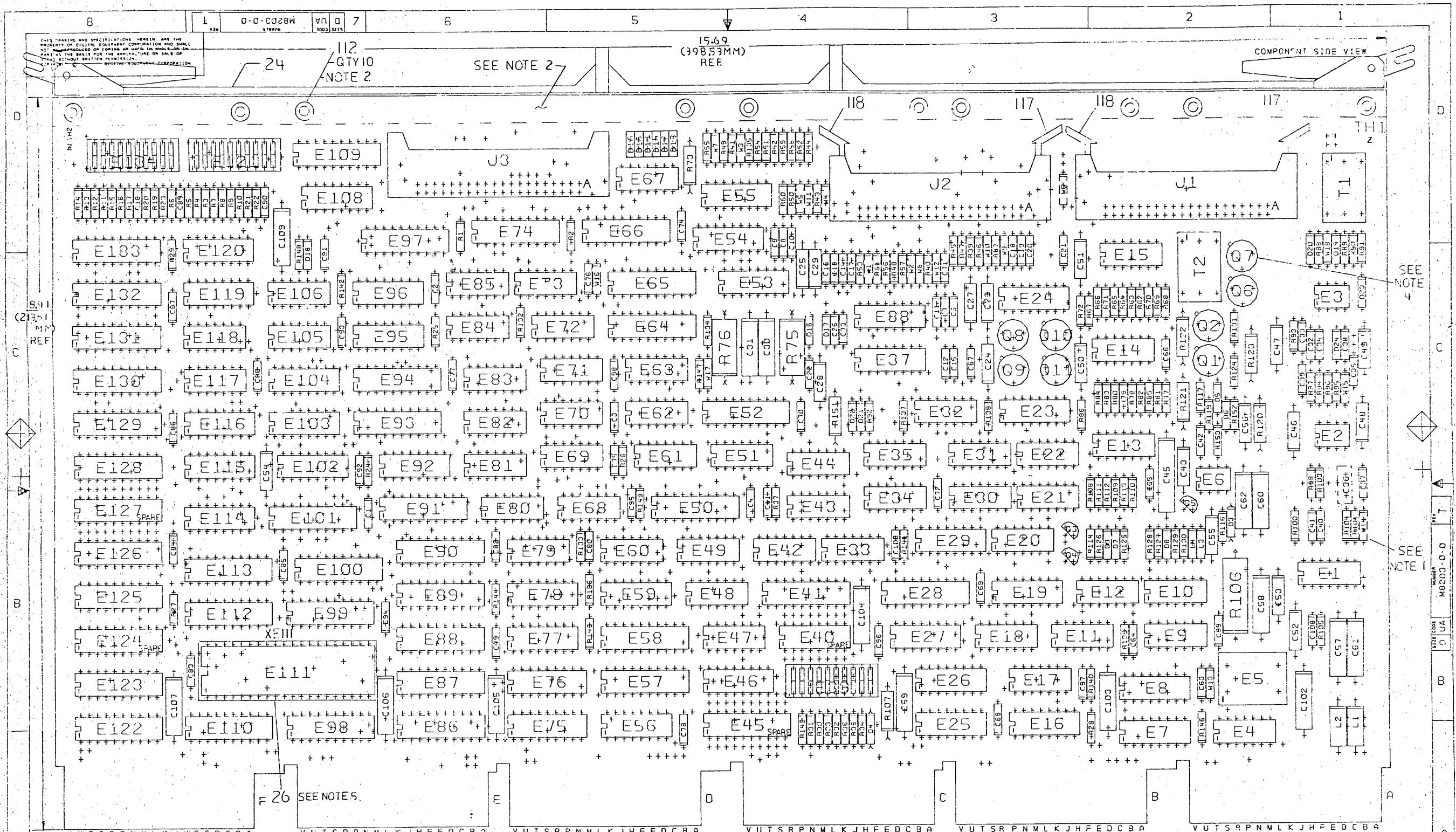
REVISIONS		
CHK	CHANGE NO.	RE

TITLE		DMPII		SIZE		CODE		NUMBER		REV.	
MICROCONTROLLER				D		CS		M8207-0-1		R	
SCALE		1		SHEET		20		OF 21		DIST.	



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## DRAWING DIRECTORY



#### NOTES

4. INSTALL ITEM 122 UNDER (QTY8)

5. THE IC. PINS MAY HAVE TO BE SHORTENED TO MEET DE  
HEIGHT SPECIFICATION AT LOCATION EIII.

C	
ETCH REV.	H-P2

SIGNATURES		DATE	digital			
DRN. B.H.FRASER		2-7-70				
CHK D. J. Fraser		200355				
MECH. ENG. <i>in edit</i>			TITLE MULTIDROP			
PROJ. ENG. <i>Bind 10</i>			LINE UNIT			
PROD. I						
SCALE 2/1		SIZE CODE	NUMBER		REV.	
SHT. 1 OF 1		0	UA		M2020-0-0	
NEXT HIGHER ASSY. K-DD-M8203-C						

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
1	1	D-MD-5013292-0-0	5013292-00	DRILL AND ETCH BOARD	1
2	2		1013466-06	100.0 MMF 50V 5%	CER 2
3	3		1013466-18	330.0 MMF 50V 5%	CER 1
4	4		1013466-28	1000.0 MMF 50V 5%	CER 1
5	5		1013466-30	560.0 MMF 50V 5%	CER 1
6	6		1013466-05	56.0 MMF 50V 5%	CER 11
7	7		1012784-00	.047 MFD 50V +80-20%	CER 41
8	8		1013466-11	.22 MFD 50V +80-20% Z5U	CER 18
9	9		1017472-00	10 MFD 35V +50-10% AL EL	15
10	10		1013466-37	1200.0 MMF 50V 5% NPO	CER 3
11	11		1013466-01	10.0 MMF 50V 5%	CER 2
12	12	BLANK	*** THIS ITEM IS NOT USED ***		
13	13		1013-66-07	220.0 MMF 50V 5%	CER 1
14	14		1013456-24	47.0 MMF 50V 5%	CER 2
15	15		1000075-00	25 MFD 25V +75-10% AL EL	1
16	16		1013466-12	2200.0 MMF 50V 10% X7R	CER 5
17	17		1100114-00	PIV= 25 IO=135 MA	10
18	18		1109943-00	VZ= 5.1 5% 1 W 1N4733A	1
19	19		1109502-00	VZ= 12.0 10% 1 W 1N4742	1
20	20		1104860-00	VZ= 3.3 5% 400 MW 1N746A	1
21	21		1114136-00	LED 16MA 5V	6
22	22		1102421-00	VZ= 6.2 5% 400 MW 1N753A	2
23	23		1209941-02	PCB, HEADER 40PIN(2X20), 100CC 90D	3
24	24		1216988-02	HANDLE, MODULE, HEX TWO EJECTORS	1
25	25		1211164-06	SW,DIP 10POS/1PST 5VDC100MA F	3
26	26		1215006-08	SKT, IC 40PIN DIP TIN SOLD	1
27	27		1300365-00	1.0 K .25 W 5.0 % CF	51
28	28		1300271-00	220.0 .25 W 5.0 % CF	10
29	29		1303179-00	*** THIS ITEM IS NOT USED ***	

CONT R1-R23,R27-R36,R92,R98,R103,

R132-R143,R146-R148

R24-R26,R37,R39-R44

REVISION HISTORY		BASIC PART NO: M8203		DRN:	A. KRAWIECKI	DATE: 09-DEC-83	D	I	G	I	T	A	L		
ENG	ECO NUMBER	REV	SECTION A. OF A.				TITLE		PARTS LIST						
PA	M8203-MK003A/3B	IH	SECTION VARIATION INDEX	CHK'D:	J. FALKOWSKI	DATE: 09-DEC-83									
IRH	M8203-MK004	IJ	[A] 00												
IRH	M8203-MK005	IK	[B]												
IRH	M8203-MK006	IL	[C]												
IFB	M8203-MK007	IM	[D]												
IFB	M8203--MK008	IN	[E]												
IFB	M8203-MK009	IP	[F]												
IML	M8203-MK012	IR	[H]												
IBB	M8203-MK013	IS	[J]												
IML	M8203-MK014	IT	[K]												
			[L]												
			[M]												
			[N]												

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37

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION		REFERENCE DESIGNATOR
				00	00	
30	30	1300447-00	4.70 K .25 W 5.0 % CF	9		R56-R61,R87,R104,R105
31	31	BLANK	*** THIS ITEM IS NOT USED ***	-		
32	32	1300432-00	3.0 K .25 W 5.0 % CF	3		R68,R69,R114
33	33	1300247-00	120.0 .25 W 5.0 % CF	6		R70-R72,R144,R145,R149
34	34	1300228-00	100.0 .50 W 5.0 % CF	1		R73
35	35	1300255-00	150.0 1.0 W 5.0 % CC	2		R75,R76
36	36	1313598-00	32.40 K .25 W 1.0 % RN55D-F10	4		R88,R91,R110,R111
37	37	1300347-00	680.0 .50 W 5.0 % CF	1		R151
38	38	1313842-00	48.70 K .25 W 1.0 % RN55D-F10	2		R93,R94
39	39	1302886-00	10.0 K .25 W 1.0 % RN55C-F 5	7		R95,R96,R108,R113,R117,R89,R90
40	40	1309418-00	24.30 K .25 W 1.0 % RN55D-F10	1		R97
41	41	1302250-00	150.0 .25 W 5.0 % CF	2		R100,R101
42	42	1302308-00	68.0 2.0 W 5.0 % CC	1		R106
43	43	1302378-00	51.0 .50 W 5.0 % CF	1		R107
44	44	1313077-00	12.80 K .25 W .10 % RN55E-B 2	2		R109,R112
45	45	1300426-00	2.70 K .25 W 5.0 % CF	1		R116
46	46	1300479-00	10.0 K .25 W 5.0 % CF	2		R125,R130
47	47	1302389-00	4.30 K .25 W 5.0 % CF	2		R119,R124
48	48	1311522-00	200.0 .25 W 5.0 % CF	2		R152,R153
49	49	1300168-00	10.0 .50 W 5.0 % CF	4		R120-R123
50	50	1300444-00	3.90 K .25 W 5.0 % CF	1		R126
51	51	1300496-00	15.0 K .25 W 5.0 % CF	2		R127,R129
52	52	1300391-00	1.50 K .25 W 5.0 % CF	1		R128
53	53	1302466-00	100.0 K .25 W 5.0 % CF	1		R131
54	54	1515018-00	2N 2905A PNP 600MW SI 40 100	1		Q2
55	55	1501881-00	DEC2219 NPN 3WC SI 30100	1		Q1
56	56	1509338-00	DEC6531B NPN 310MW SI 40 90 P	2		Q3,Q4
57	57	1503409-00	DEC6534D PNP 310MW SI 40 90	1		Q5
58	58	1516150-00	VN 35AK FET 50MW	6		Q6-Q11
59	59	1602723-00	1000 UH 10% 125MA	2		L1,L2
60	60	1612946-01	33.0 UM 10% Q MIN=45@2.5MHZ	2		L3,L4
61	61	1616045-00	XFMR,PULSE,RATIO 1:1,1500PV	1		T2
62	62	1616303-00	PULSE XFMR,RATIO 1:1:1	1		T1
63	63	1811660-00	OSCILLATOR, XTAL 20,000 MHZ	1		E5
64	64	1914214-00	LS374 FF-D OCTAL EDGE TRIG	13		E86,E88,E109,E122,E123,E125, E126,E128-E133
65	65	1913462-00	74S240 OCTAL BUFFER,INVERTI	2		E74,E97
66	66	1911675-00	74S138 DECODER/DEMUX 3-8 LI	3		E59,E108,E119
67	67	1912863-00	LS273 FF-D OCTAL W/CLEAR	6		E58,E66,E87,E90,E91,E94
68	68	1912697-00	LS174 FF-D HEX W/CLEAR	2		E92,E96
69	69	1910534-00	74S04 INVERTER GATE-HEX 1I	1		E17
70	70	1912824-00	LS74 FF-D DUAL,EDGE TRIGG	7		E4,E62,E63,E58,E80,E84,E118
71	71	1912849-00	LS161 COUNTER,SYNCHR,4BIT	1		E27
72	72	1913294-00	93S16 COUNTER,SYNCH UP BIN	3		E7,E8,E16
73	73	1910956-00	74S151 MUX 1 OF 8	1		E26
74	74	1912847-00	LS157 MUX 1 OF 2(QUAD)	2		E46,E95
75	75	1910548-00	74S157 MUX 1 OF 2 (QUAD)	1		E120
76	76	1915193-00	LS244 DRIVER,LINE,OCTAL,T	9		E64,E65,E89,E98,E99,E101,E110,

! ! ! ! ! ! !	! ! ! ! ! ! !	! ! ! ! ! ! !	! ! ! ! ! ! !	! ! ! ! ! ! !	! ! ! ! ! ! !	! ! ! ! ! ! !
D	I	G	I	T	A	L
I	I	I	I	I	I	I
I	I	I	I	I	I	I

TITLE MULTIDROP LINE UNIT SECTION A OF A

SIZE	CODE	DOCUMENT NUMBER	REV
K	PL	M8203-0-0	T

## PARTS LIST

SHEET A3 OF A4

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
77	77	1912816-00	LS32 OR GATE-QUAD 2IN,POS	4	CONT E112,E113
78	78	1913777-00	LS240 DRIVER,LINE,OCTAL,T	4	E20,E51,E81,E83
79	79	1915125-00	3691 DRIVER,QUAD,EIA RS-	2	E28,E50,E52,E93
80	80	1912742-00	75112 DRIVER,DUAL LINE	1	E37,E53
81	81	1915851-00	26LS31 DRIVER,QUAD,EIA RS	1	E13
82	82	1915694-00	3692 DRIVER,QUAD,EIA RS-4	1	E23
83	83	1915123-02	26LS32-2 RECEIVER,LINE,12K T	3	E24
84	84	1914091-00	3603 RECEIVER,LINE,DUAL	3	E38,E54,E55
85	85	1910550-00	74S174 FF-D HEX	2	E1,E14,E15
86	86	1910544-00	74S74 FF-D DUAL,EDGE TRIGG	7	E77,E78
87	87	1910741-00	7406 INVERTER GATE-HEX 1I	1	E9,E10,E18,E35,E47,E61,E11
88	88	1912805-00	LS08 AND GATE-QUAD 2IN,PO	6	E67
89	89	1912808-00	LS11 AND GATE-TRIPLE 3IN	3	E31,E33,E60,E72,E79,E82
90	90	1912821-00	LS54 A-O-I GATE,3-2-2-3IN	1	E69,E105,E117
91	91	1910536-00	74S10 NAND GATE-TRIPLE 3IN	2	E85
92	92	1912801-00	LS02 NOR-GATE-QUAD 2IN	1	E48,E106
93	93	1910537-00	74S11 AND GATE-TRIPLE 3INP	1	E73
94	94	1910011-00	7486 X-OR GATE-QUAD 2INPU	1	E49
95	95	1911712-00	74S51 AND-OR GATE-INVERT D	2	E43
96	96	BLANK	*** THIS ITEM IS NOT USED ***	-	E32,E44
97	97	1912850-00	LS164 SHIFT REG. 8BIT SERI	3	
98	98	1910735-02	318 OP AMP	3	E21,E30,E42
99	99	1910532-00	74S00 NAND GATE-QUAD 2IN	2	E2,E6,E3
100	100	1910545-00	74S112 FF-JK DUAL,EDGE TRIG	2	E12,E34
101	101	1912803-00	LS04 INVERTER GATE,HEX	1	E19,E25
102	102	1912853-00	LS175 FF-D QUAD	1	E22
103	103	1912799-00	LS00 NAND-GATE-QUAD 2IN,P	2	E29
104	104	2111188-01	3341-01 64X4 FIFO MEMORY	6	E70,E71
105	105	2112517-01	RECIEVER/TRANSMITTER	1	E102-E104,E114-E116
106	106	2304381-00	B1-01	1	E111
107	107	2303881-00	B1-01	1	E41
108	108	23036F1-00	F1-01	1	E100
109	109	23037F1-00	F1-01	1	E76
110	110	2304281-00	B1-01	1	E75
111	111	9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1	E57
112	112	9009000-00	EYELET,ROLLED 0.1210DX0.156	8	W7-W10,W12,W13,W17,W18
113	113	1013466-31	15.0 MMF 50V 5% CER	10	
114	114	1301890-00	560.0 .25 W 5.0 % CF	1	C42
115	115	1101938-00	VZ= 2.4 5% 400 MW 1N4370A	2	R85,R86
116	116	1301422-00	7.50 K .25 W 5.0 % CF	1	D22
117	117	1209941-03	PCB,HEADER LATCH	11	R45-R55
118	118	1209941-04	PCB,HEADER LATCH	2	
119	119	1316115-00	35.70 .25 W 1.0 % RN55D-F10	4	R81-R84
120	120	1316736-00	374.0 .25 W 1.0 % RN55D-F10	4	R77-R80
121	121	1312928-00	51.0 .25 W 5.0 % CF	6	R62-R67
122	122	BLANK	*** THIS ITEM IS NOT USED ***	-	
123	123	9007201-00	74S163 COUNTER,SYNCH UP/DOW	1	E56
		1914082-00			

TITLE

MULTIDROP LINE UNIT

SECTION A OF A

SIZE CODE DOCUMENT NUMBER REV

K PL M8203-0-0 T

AUTOMATED BY PRTLST.3L(360)

P A R T S   L I S T

SHEET A4 OF A4

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION

00

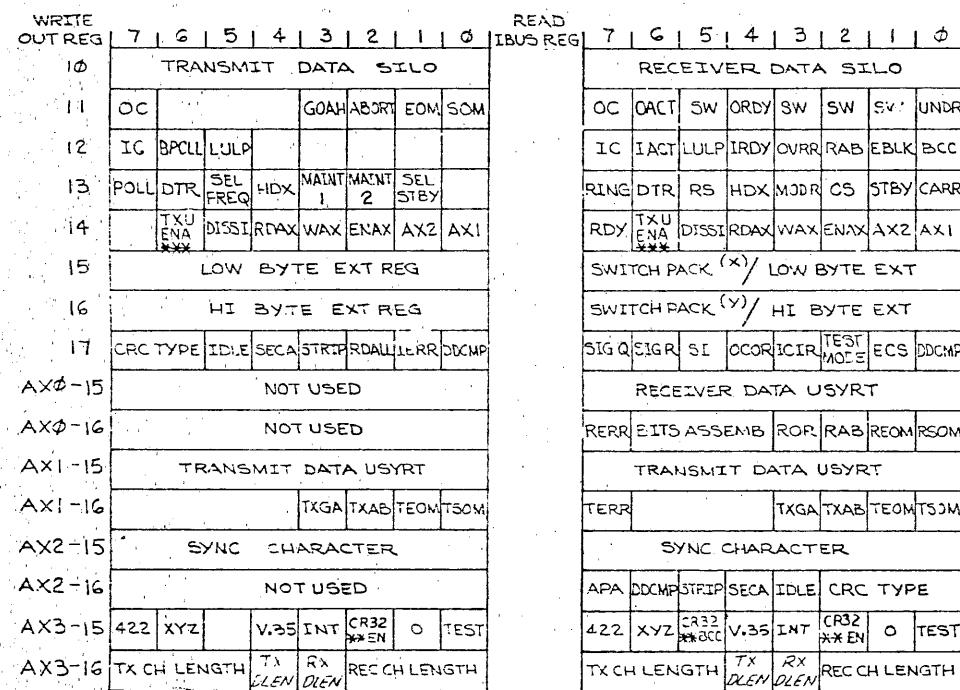
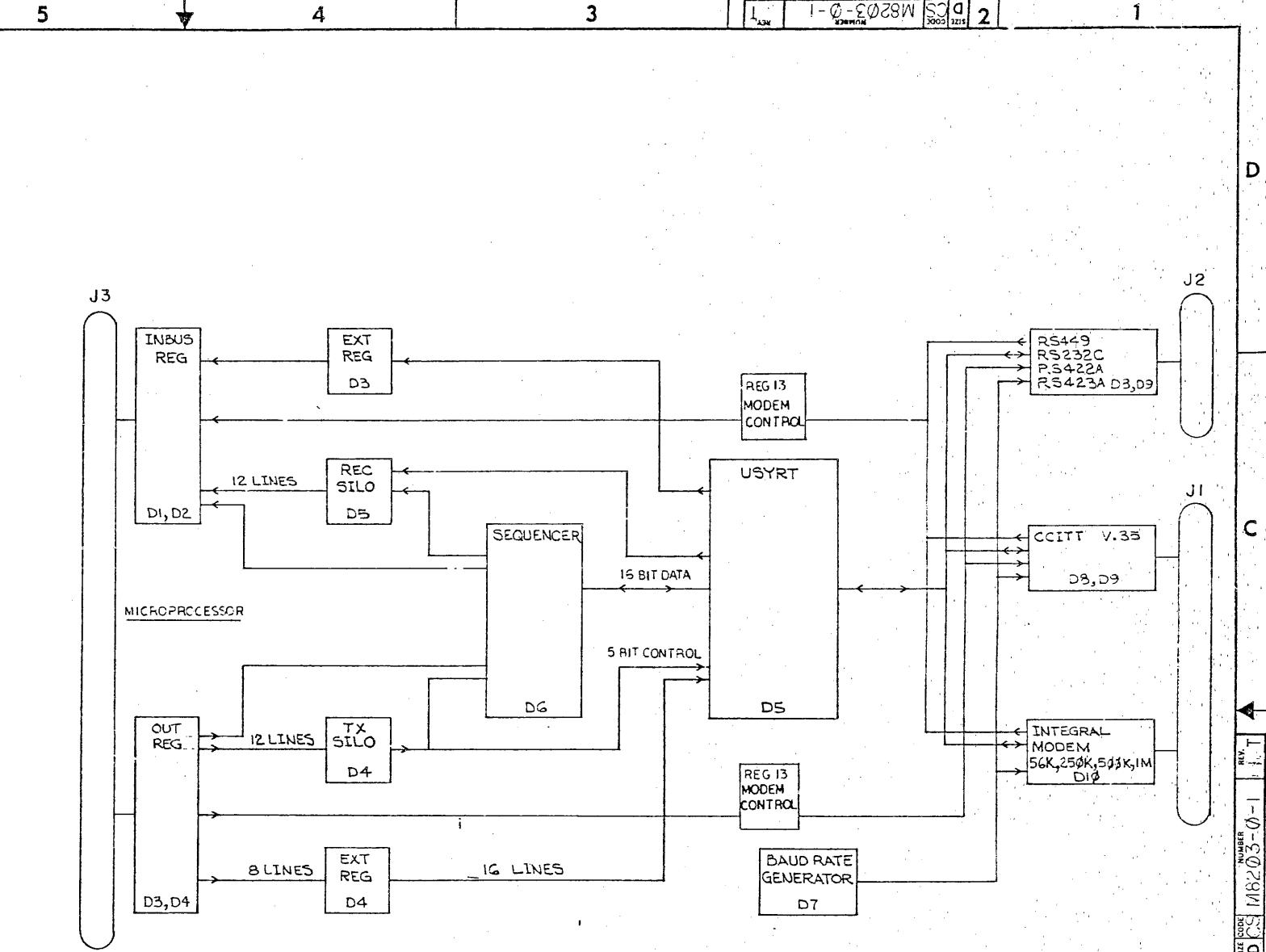
REFERENCE DESIGNATOR

124 124 BLANK

9105740-55

\*\*\* THIS ITEM IS NOT USED \*\*\*

TITLE		SECTION A OF A	ISIZE	CODE	DOCUMENT NUMBER	REV									
M	D	I	I	G	I	I	T	A	L	MULTIDROP LINE UNIT	K	P	L	M8203-0-0	T



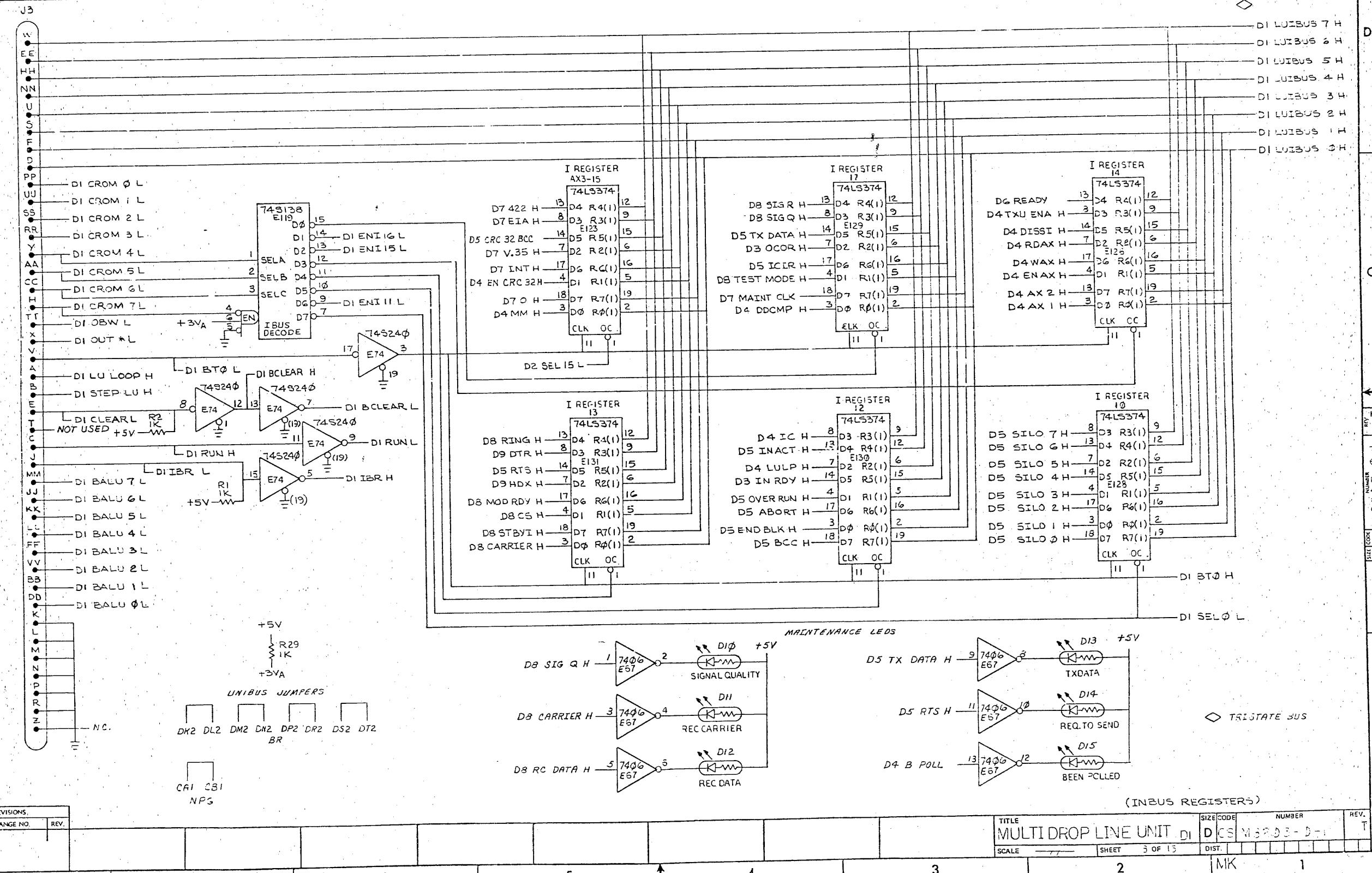
## NOTE

1. AX REGISTERS ARE INDIRECTLY ADDRESSED BY USING AX1+AX2 FOR ADDRESS, WAX FOR WRITE, RDAX FOR READ, REGISTERS 15 + 16 FOR DATA, ENAX GOING TO A ONE TO START THE OPERATION AND RDY TO TELL WHEN THE OPERATION IS COMPLETE.
2. BIT MAP AS SEEN BY THE DMRII AND DMP11 MICRO PROCESSOR.
3. DELETED.
4. \*\*\* OPTIONAL WHEN CRC32 CHIP IS INSTALLED.
5. \*\*\* BIT 6 OF REG 14 IS DEFINED DIFFERENTLY DEPENDING ON THE STATE OF BITS:  
BIT 5=0 BIT 6 WILL LOCK RTS ON.  
BIT 5=1 BIT 6 WITH ENABLE USRT TRANSMITTER.

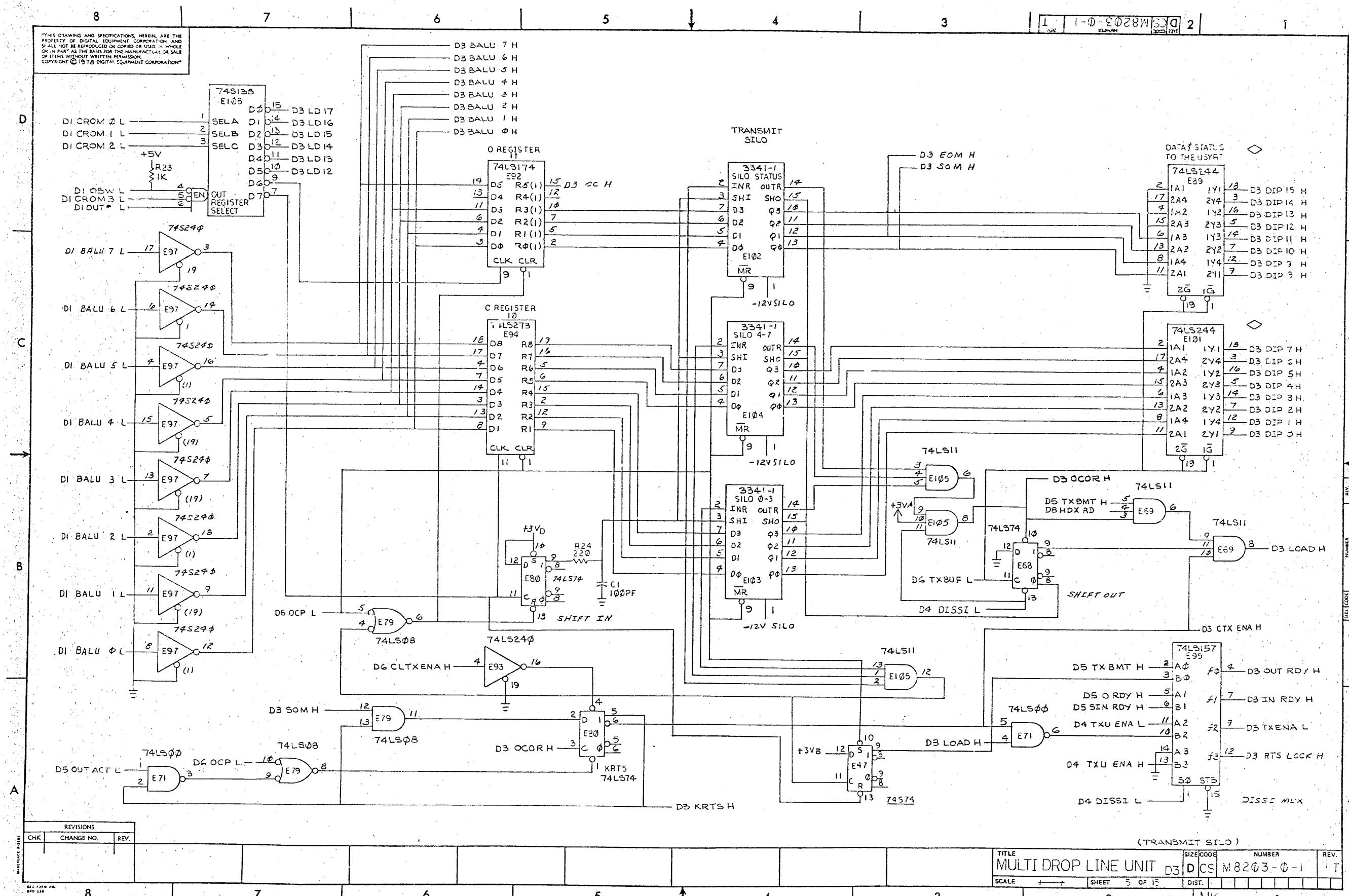
DRN. S-1000	12-3-9	FIRST USED ON	DM P11-AD	digital
CHK'D: <i>John</i>	4545			
ENG. P. 1000	12-3-9	TITLE		
PROL. ENG. A. 1000	12-3-9	MULTI DROP		
PROD. P. 1000	12-3-9	LINE UNIT		
NEXT HIGHER ASSY.				
K-DD-M8203-2	SIZE	CODE	NUMBER	REV.
SCALE NONE	D	CS	M8203-0-1	T
SHEET 1 OF 15	DIST.			

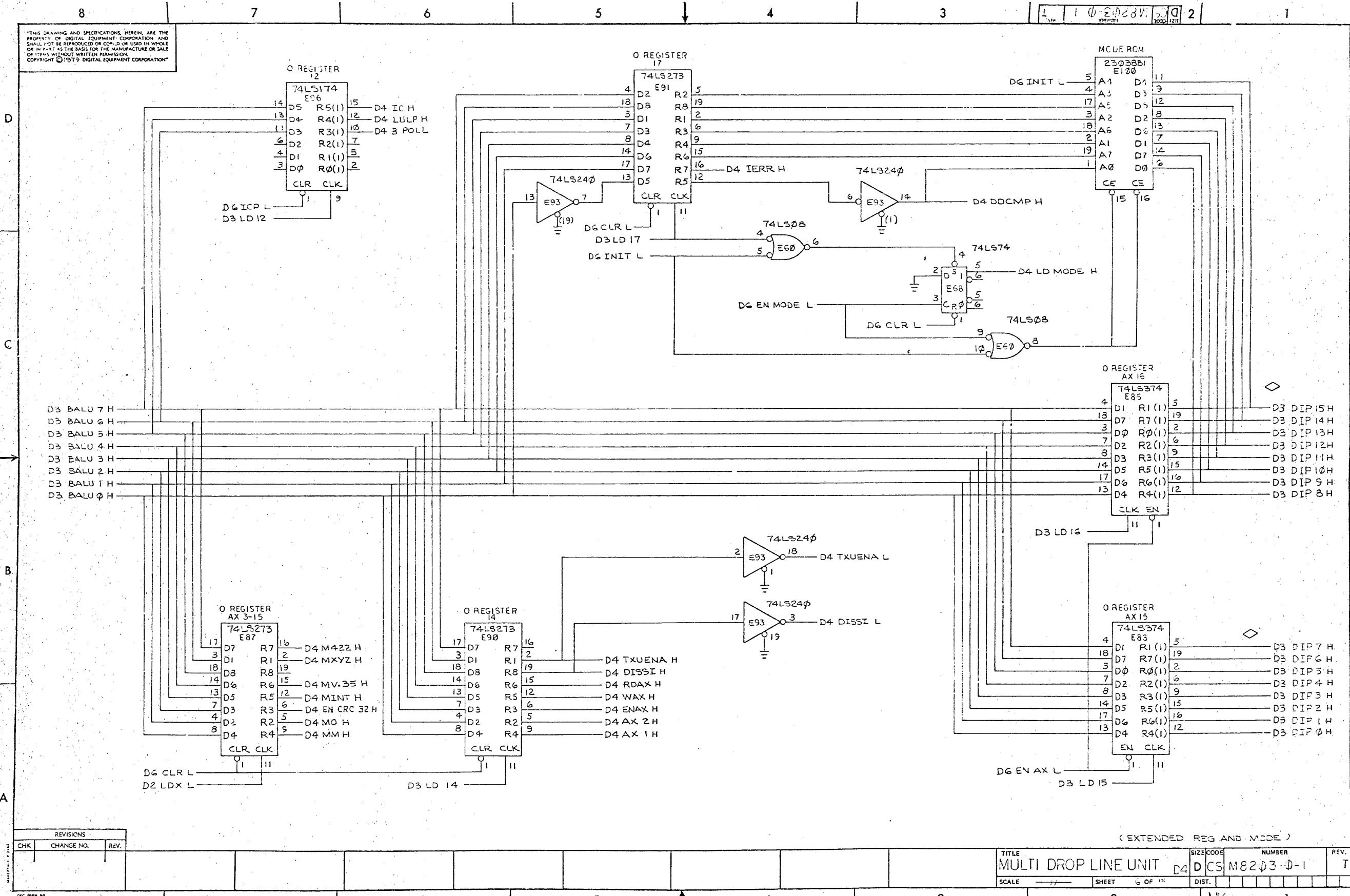


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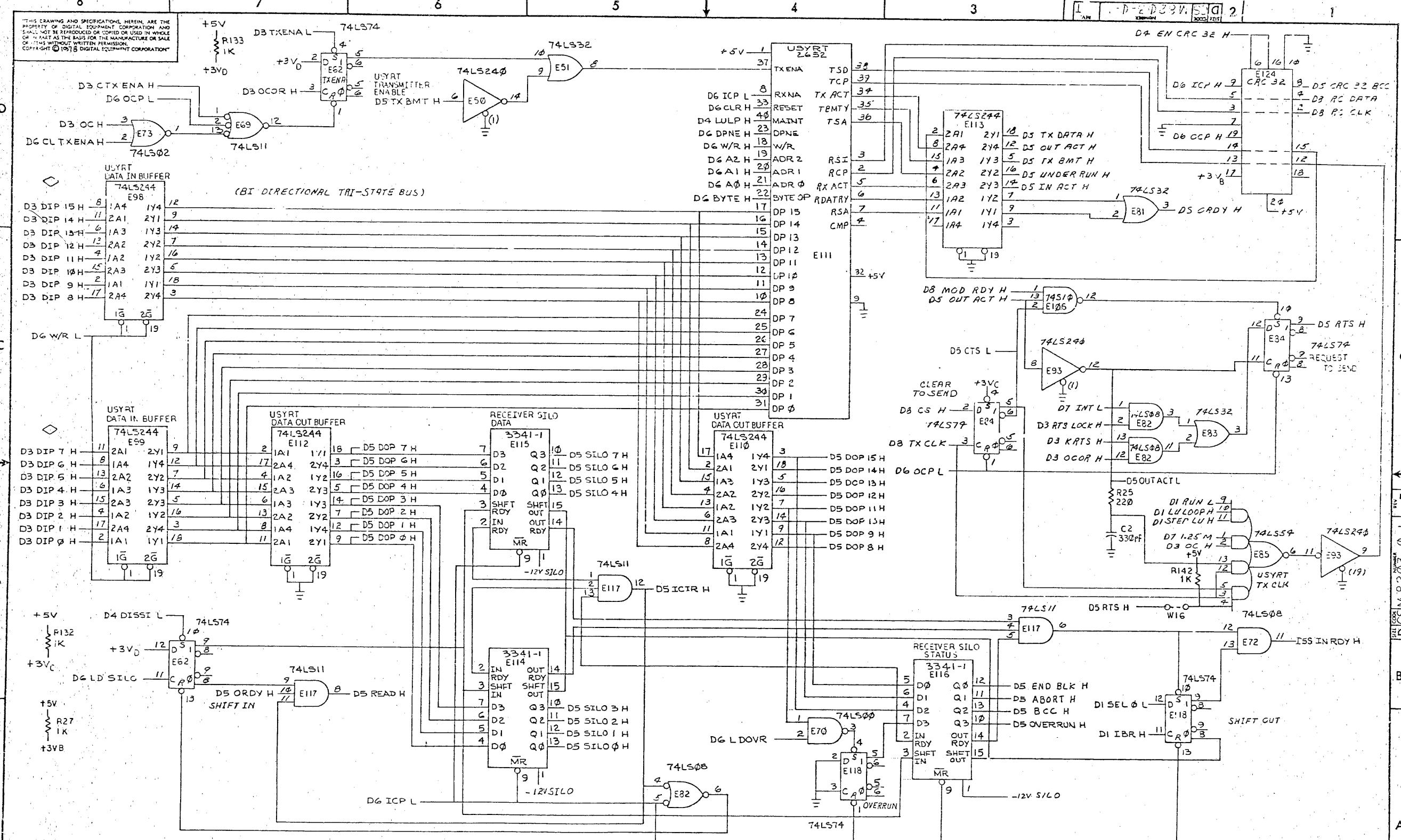








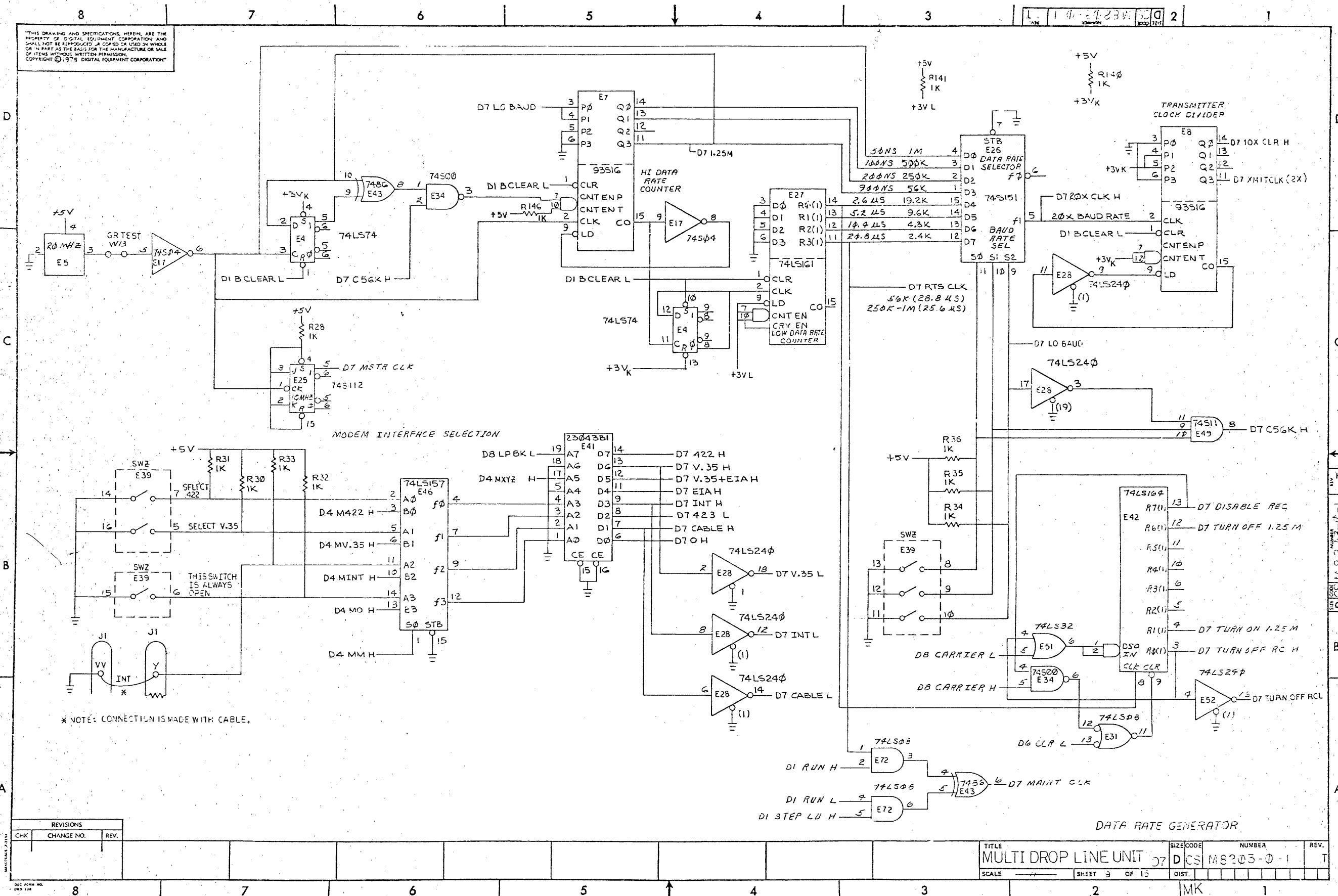
REVISIONS	
CHK	CHANGE NO.



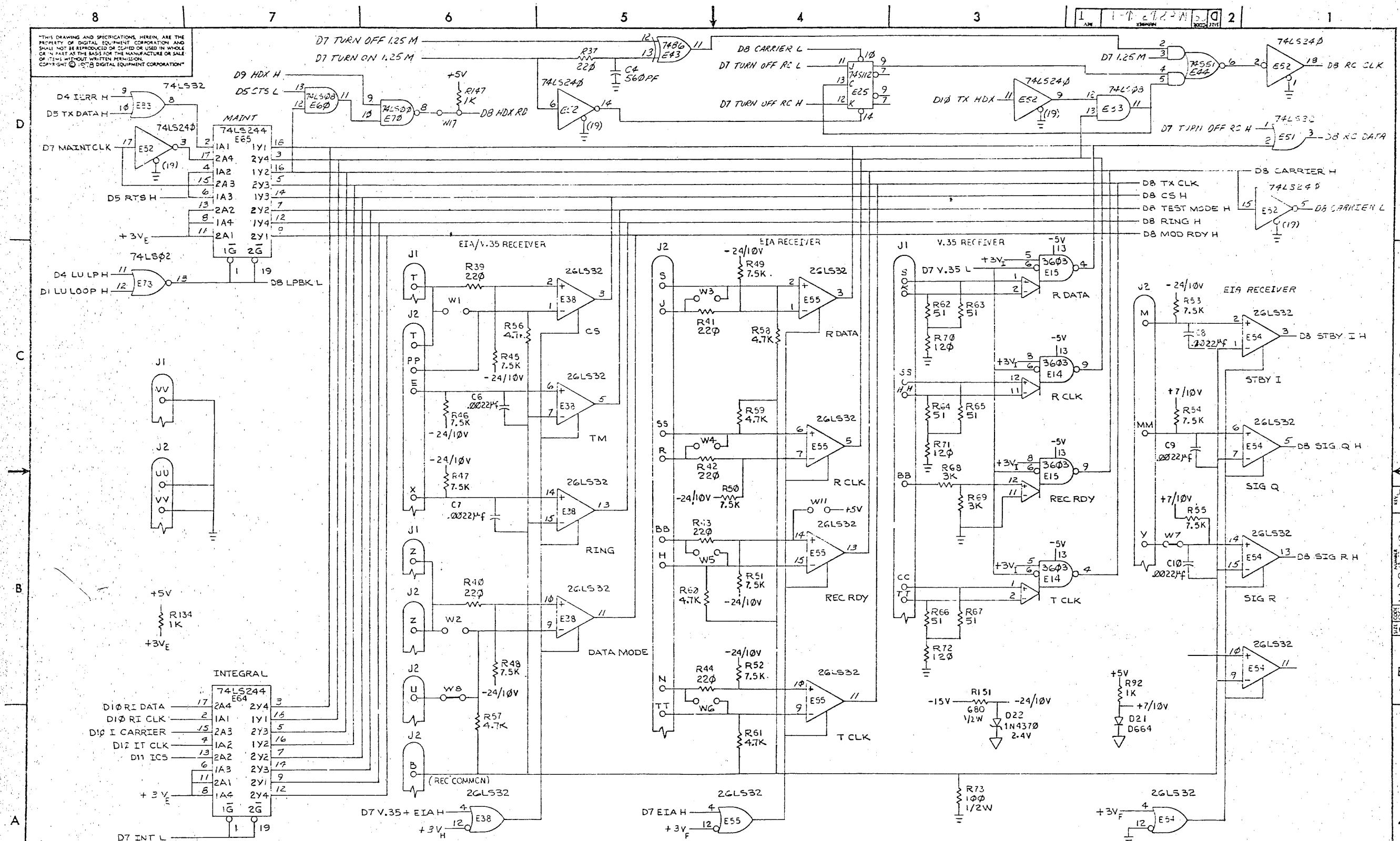
REVISIONS		
CHK	CHANGE NO.	REV

USYRT AND REC SILO				
TITLE		SIZE CODE	NUMBER	REV.
MULTI DROP LINE UNIT		D CS	MS202-0-1	T
SCALE	SHEET 7 OF 15	DIST.		
		2	MK	1



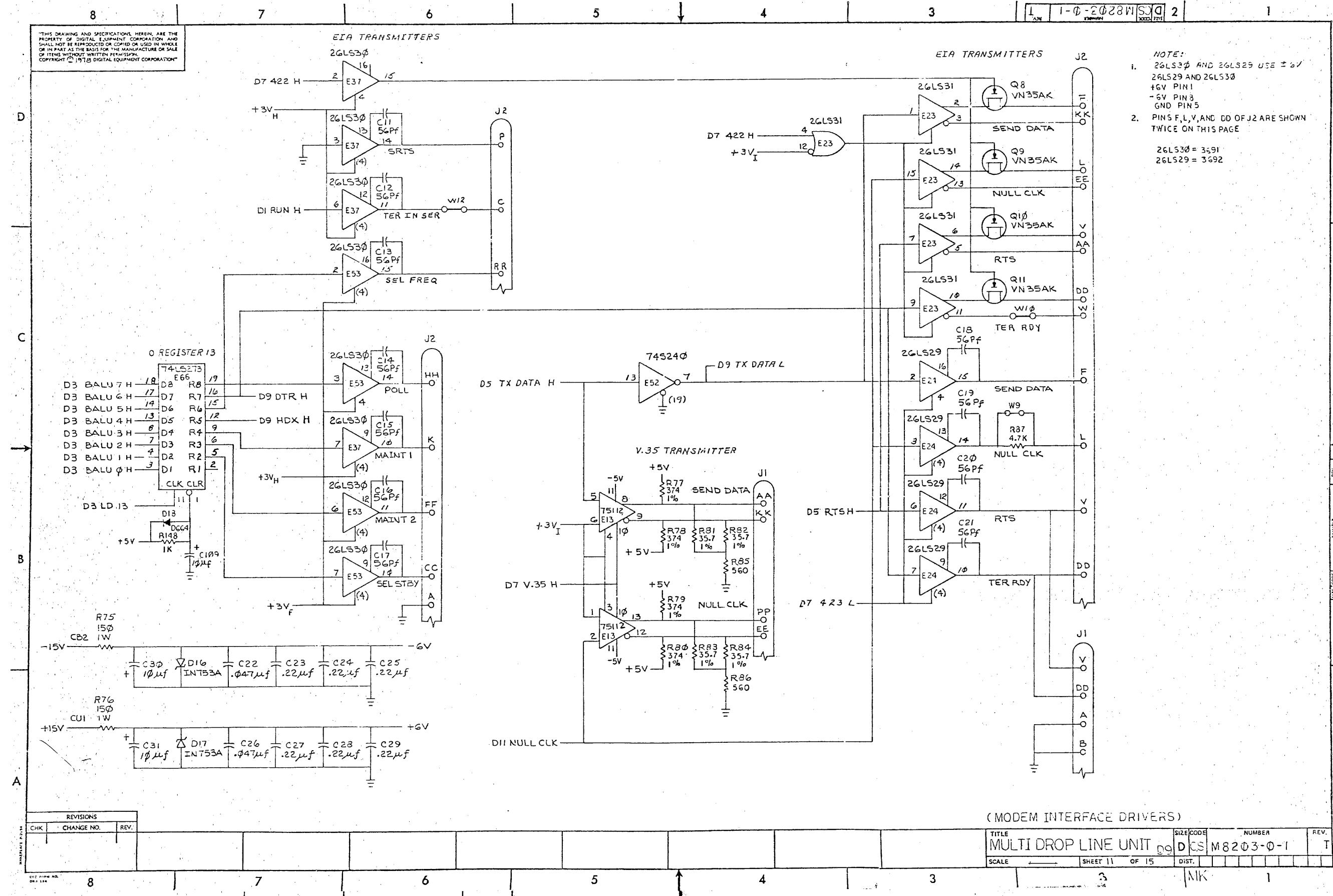


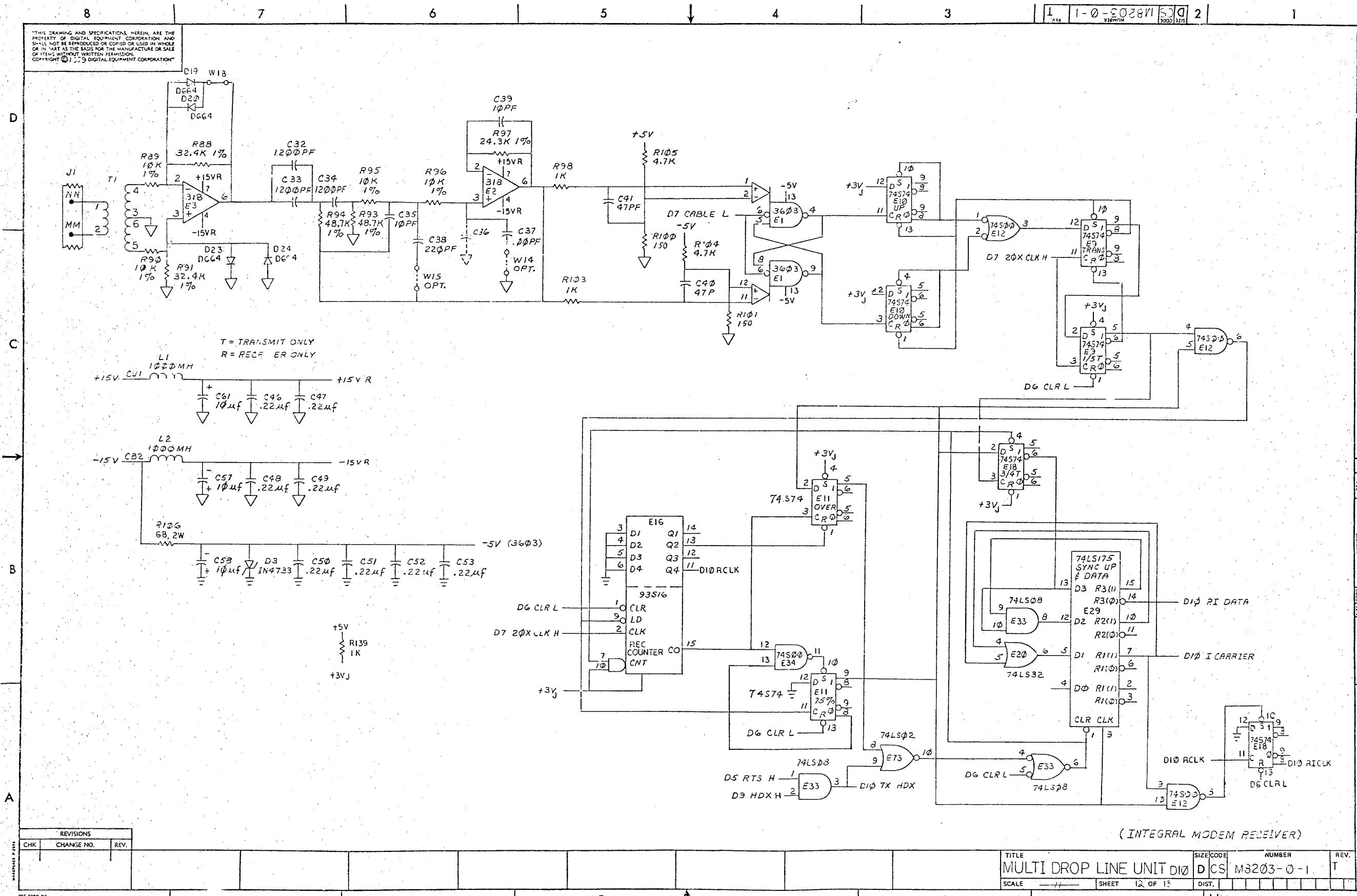
REVISIONS	
CHK	CHANGE NO.



REVISIONS	
CHK	CHANCE NO.

( MODEM INTERFACE RECEIVERS )			
LINE UNIT	SIZE CODE	NUMBER	REV.
D8	DICS	118203-0-1	T
SHEET 16 OF 15	DIST.		





(INTEGRAL MODEM RECEIVER)

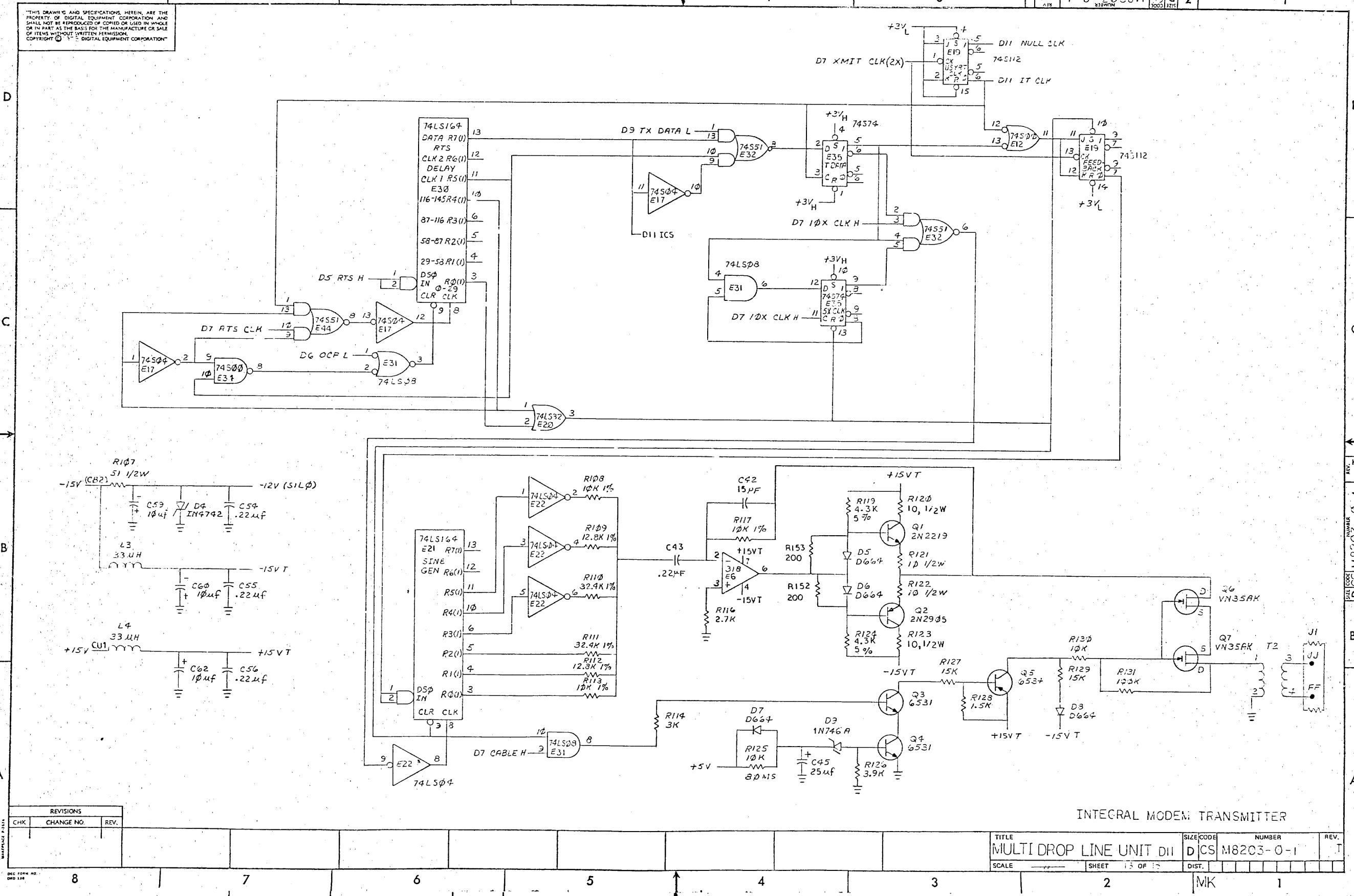
REVISIONS		
CHK	CHANGE NO.	R

MANUFACTURE P-2838

ONE FORM, NO. 1

TITLE <b>MULTI DROP LINE UNIT DI0</b>	SIZE CODE <b>D C S</b>	NUMBER <b>M8203-0-1</b>	REV. <b>T</b>
SCALE <b>11</b>	SHEET <b>12 OF 13</b>	DIST. <b>14</b>	

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D1	D2	D3	D4	D5	D6	D7	D8	D9	D10	D11
BALU 0 L	1	1								
BALU 1 L	1	1								
BALU 2 L	1	1								
BALU 3 L	1	1								
BALU 4 L	1	1								
BALU 5 L	1	1								
BALU 6 L	1	1								
BALU 7 L	1	1								
B CLEAR L	1			2	5					
BT0 H	7	3								
BT0 L	2									
CROM 0 L	1	1								
CROM 1 L	1	1								
CROM 2 L	1	1								
CROM 3 L	1	1								
CROM 4 L	2									
CROM 5 L	2									
CROM 6 L	2									
CROM 7 L	2									
CLEAR L	3									
ENI 11 L	1	1								
ENI 15 L	1	2								
ENI 16 L	1	2								
IBR H	1		1							
IBR L	3									
LU LOOP H	1	1	1	1						
LUIBUS 0 H	7	5								
LUIBUS 1 H	7	5								
LUIBUS 2 H	7	5								
LUIBUS 3 H	7	5								
LUIBUS 4 H	7	5								
LUIBUS 5 H	7	5								
LUIBUS 6 H	7	5								
LUIBUS 7 H	7	5								
OBW L	1	2	1							
OUT* L	1	1								
RUN H	2			1	1					
RUN L	1		1	1						
SEL 0 L	2		1							
STEP LU H	1		1	1						
+3VA	2	4								
B CLEAR H	2		1							

D2	D1	D2	D3	D4	D5	D6	D7	D8	D9	D10	D11
LDX L	1	1									
SEL 15 L	1	1									
D3											
BALU 0 H		3	5		1						
BALU 1 H		3	5		1						
BALU 2 H		3	5	1	1						
BALU 3 H		3	5		1						
BALU 4 H		2	5		1						
BALU 5 H		2	6		1						
BALU 6 H		2	6		1						
BALU 7 H		3	6		1						
CTX ENA H		3	1								
DIP 0 H		1	1	1							
DIP 1 H		1	1	1							
DIP 2 H		1	1	1							
DIP 3 H		1	1	1							
DIP 4 H		1	1	1							
DIP 5 H		1	1	1							
DIP 6 H		1	1	1							
DIP 7 H		1	1	1							
DIP 8 H		1	2	1							
DIP 9 H		1	2	1							
DIP 10 H		1	2	1							
DIP 11 H		1	2	1							
DIP 12 H		1	2	1							
DIP 13 H		1	2	1							
DIP 14 H		1	2	1							
DIP 15 H		1	2	1							
EOM 0 H		2	1								
IN RDY H		1	1								
KRTS H		3	1								
LD 12		1	1								
LD 13		1									
LD 14		1	1	1							
LD 15		1	1	1							
LD 16		1	1								
LD 17		1	2								
LOAD H		2	1								
OC H		1	1	2	2						
OCOR H		1	4	2							
OUT RDY H		1	1								
RTS LOCK H		1	1								
SOM H		3									
TX ENA L		1	1								

D4	D1	D2	D3	D4	D5	D6	D7	D8	D9	D10	D11
AX 1 H	1	1		1	1						
AX 2 H	1	1		1	1						
B POLL		1		1							
DDCMP H	1		2								
DISSI H	1		2								
DISSI L		2	1	1							
ENAX H	1	1	1								
EN CRC 32 H	1		1	2							
IC H	1		1	2							
IERR H		1		1							
LD MODE H		1	1	1							
LULP H	1		1	1							
M422 H		1		1							
MINT H		1		1							
M 1 H	1		1	1							
MO H		1		1							
MV.35 H		1		1							
MXYZ H		1		1				</			

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D7	D1	D2	D3	D4	D5	D6	D7	D8	D9	D10	D11
1.25M				1	4	1					
422 H	1				1	2					
423 L					1	1					
CABLE H					2						
CABLE L					1	1					
56K H					2						
DISABLE REC					2						
EIA H	1				1	1					
+3VK						7					
INT H	1				2						
INT L					1	1	2				
LO BAUD						5					
MAINT CLK	1				1	2					
MSTR CLK					4	1					
						4					
23X CLK H					2		3	1			
ATS CLK					3						
TURN OFF 1.25M					1	1					
TURN OFF RC L					1	1					
TURN OFF RC H					3	1					
TURN ON 1.25M					1	1					
V.35 H	1				2	1					
V.35 L					1	2					
V.35+EIA H					1	1					
XMIT CLK					1		2				
0 H	1				1						
+3VL					4		5				
10X CLK H					1	2					
D8											
CARRIER H	2					1	6				
CARRIER L						1	2				
CS H	1					1	3				
HDX RD						1					
LP BEL L						1	3				
MOD RDX H	1				1	3					
RC CLK					3		1				
RC DATA	1				3		1				
RING H					1		3				
SIG Q H	2					1					
SIG R H	1					1					
STBY I H	1					1					
TEST MODE H	1					3					
TX CLK					3	4					
+3VE						7					
+7/10V						4					
-24/10V						11					

D9	D1	D2	D3	D4	D5	D6	D7	D8	D9	D10	D11
DTR H	1								3		
HDX H	1								1	1	
TX DATA L									3	1	
D10											
I CARRIER								1	3		
+3VJ									7		
RJ CLK								1	1		
RI DATA								1	1		
TX HDX								1	2		
RCLK									2		
D11											
ICS								1	3		
ITCLK								1	4		
NULL CLK								3	1		

REVISIONS		
CHK	CHANGE NO.	REV.

MAINTENCE PARTS

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digital

USED ON OPTION/MODEL		DRN. <i>JH Jr. ....</i>	DATE <i>27-444-31</i>	TITLE <i>M8203 TEST CONNECTOR J1</i>	
DMP11-AD		CHFD <i>J. Faliszowski HJ</i>	DATE <i>27 MAR 81</i>	DOCUMENT NUMBER	
DMR11		DES. ENG. <i>R HARRINGTON</i>	DATE <i>3/27/81</i>	SIZE <b>B</b>	CODE <b>DD</b>
		RESP. ENG. <i>P. COLLIETTE</i>	DATE <i>3/27/81</i>	NUMBER <i>M8254-0</i>	
		MFG. ENG. <i>BILL BROOK</i>	DATE <i>3/27/81</i>	REV <i>B</i>	
				SHEET 1	OF 1

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8 7 6 5 4 3 2 1

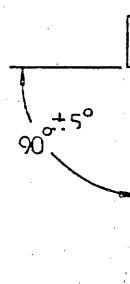
COMPONENT SIDE VIEW

D

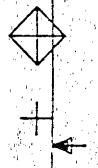
C

B

A

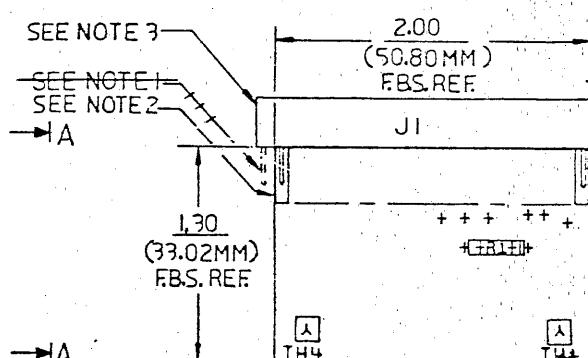


VIEW A-A



B

B



2AC20

A

NOTES:

1. REMOVE CONNECTOR TO REMOVE FOUR (+) PINS TWO (2) AT EACH END OF THE CONNECTOR PRIOR TO ASSEMBLY TO THE BOARD.

2. OUTER FINGERS MAY BE CUT IN SHEARING PROCESS.

STEP E → Y AXIS 1.3 STEP 7 TIMES  
REPEAT → X AXIS 2.0 STEP 7 TIMES

CHK	CHANGE NO	REV	ORIGINATED	1	2	3	4	5	6	7	8
10	H3254	MK1	4								
10	H3254	MK2	8								
10	H3254	MK2	10								

NOTES CONT:

3. SOLDER J1 TO PC BOARD.

ETCH REV. B-P2

SIGNATURES	DATE	digital
DRN	3/17/81	
CHK	0	
MECH. ENG.	3/17/81	
PROJ. ENG.	3/17/81	
PROD.	3/17/81	
SCALE 2/1	SIZE CODE	NUMBER
SHT. 1 OF 1	0	UA H3254-0-0 B
NEXT HIGHER ASSY. B-00-H3254-0-0		

8 7 6 5 4 3 2 1

MK 1 WO#301566